

Model Name: GA-H81M-D2W WG

SHEET TITLE Revision 1.0

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS X1 *2 SLOT
16	PCI SLOT (NA)
17	ITE 8620 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC887-VD2
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX
27	VCORE ISL95812_1

SHEET TITLE

28	VCORE ISL95812_2
29	RT8120_DDR POWER
30	LPT
31	DVI
32	IT8892E (NA)
33	USB3 VL805

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Gigabyte Technology			
Title			
Cover Sheet			
Size Custom	Document Number	GA-H81M-D2W WG	Rev 1.0
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Component value change history

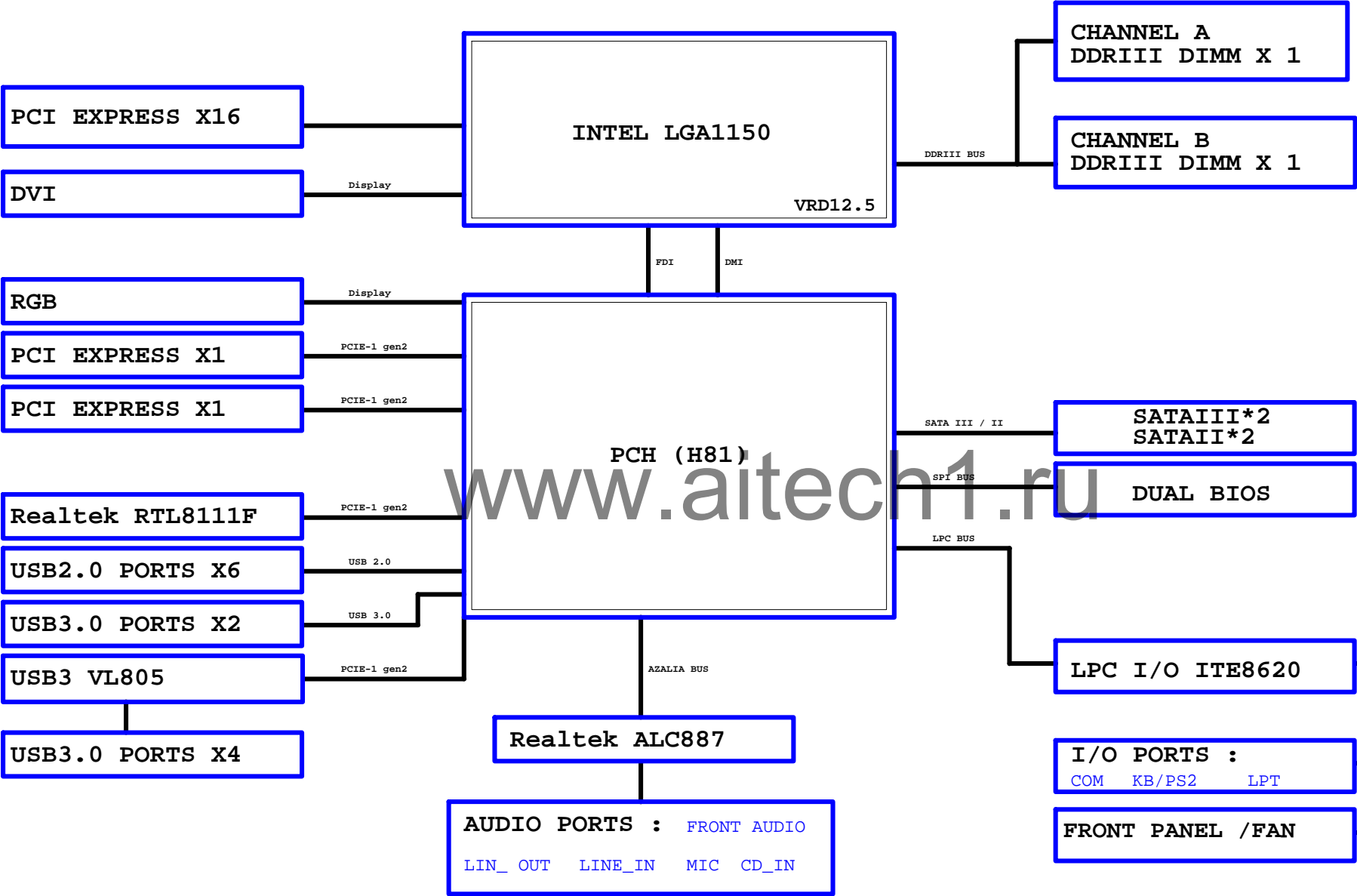
Circuit or PCB layout change

[illegible]

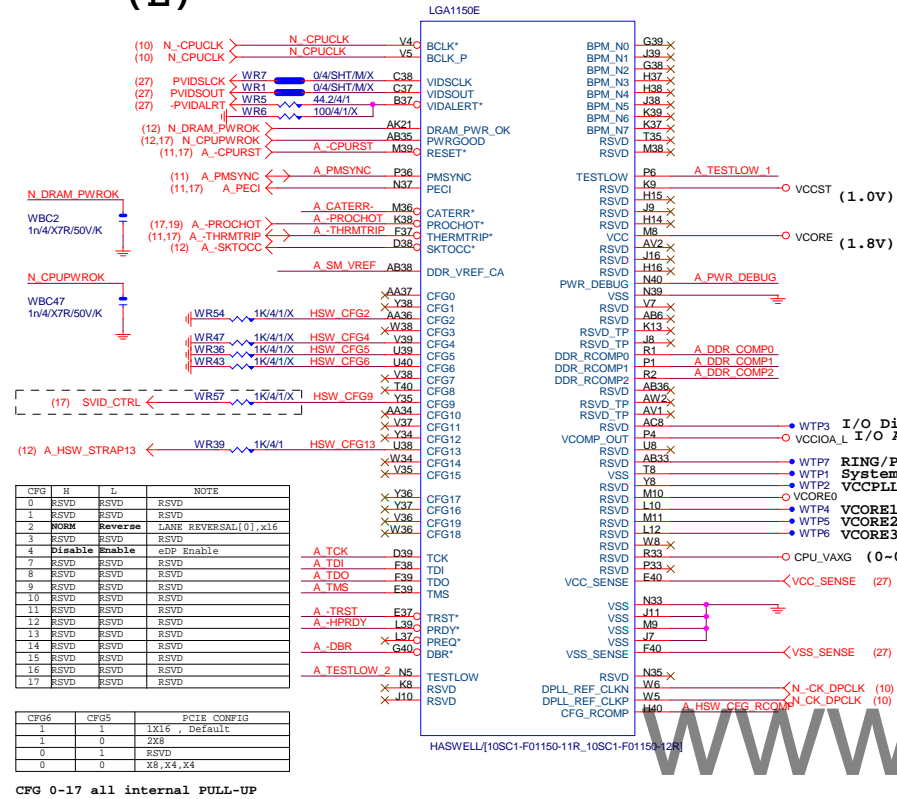
PCB : S4VNB (精成,全成信,伊利安達)
S:單文
4:四層板
V:第二層是VCC
N:咖啡色
B:製程

<i>Gigabyte Technology</i>			
Title			
BOM & PCB MODIFY HISTORY			
Size Custom	Document Number	GA-H81M-D2W WG	Rev 1.0
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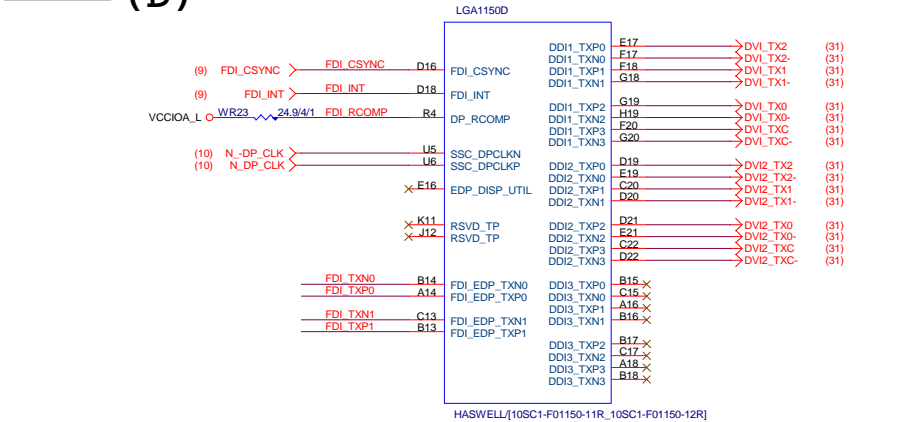
BLOCK DIAGRAM



LGA1150 (E)



LGA1150 (D)

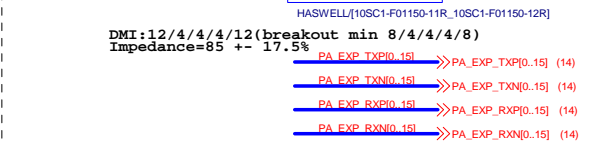


FDI:12/4/5/4/12(breakout min 6/4/4/4/6)
Impedance=85 +- 17.5%

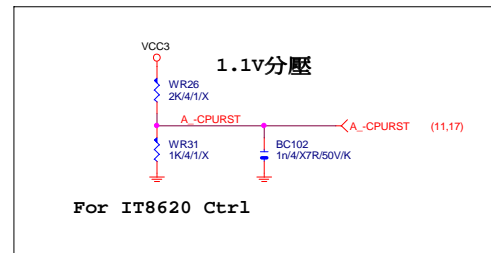
FDI TXN0_11 >>> FDI_TXN[0..1] (9)
FDI TXN0_11 >>> FDI_TXN[0..1] (9)

LGA1155 (C)

PCIEX16:16/5/5/5/16(breakout min 10/4/4/4/10)
Impedance=80 +- 17.5%



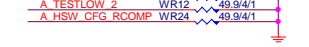
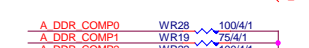
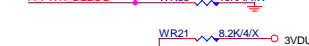
-CPURST



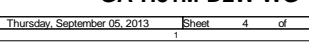
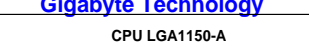
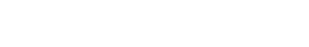
CPU SVID



CPU PU/PD



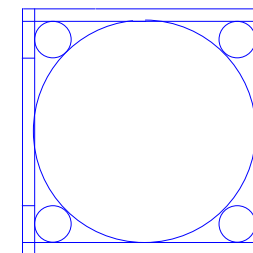
SM REF



LGA1150A

HASWELL/I10SC1-F01150-11R I0SC1-F01150-12R

LGA1150B

HASWELL/10SC1-F01150-11R 10SC1-F01150-12RCR
CPU RETAINTION/X

LGA1150

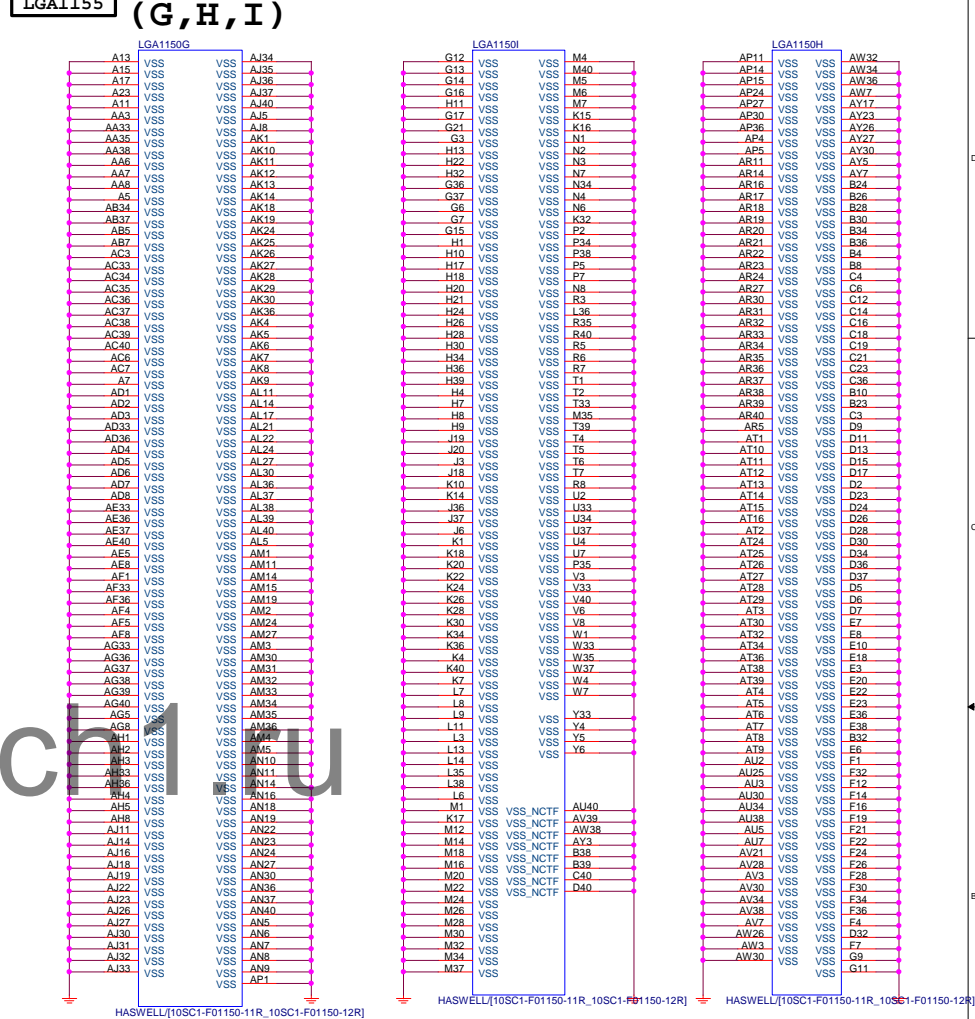


ILM_BP/1156/CSP/ILM_BP/1156/CSP/[12KRC-0F0001-52R_12KRC-0F0001-51R]

DDR BUS

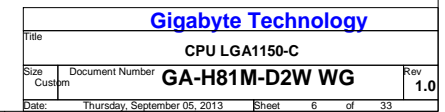
(7)	MODT_A[0..1]	⇐	MODT_A0_1
(8)	MODT_B[0..1]	⇐	MODT_B0_1
(7)	MDA[0..63]	⇐	MDA0_63
(8)	MDB[0..63]	⇐	MDB0_63
(7)	DQSA[0..7]	⇐	DQSA0_7
(7)	-DQSA[0..7]	⇐	-DQSA0_7
(7)	MAAA[0..15]	⇐	MAAA0_15
(8)	MAAB[0..15]	⇐	MAAB0_15
(8)	DQSB[0..7]	⇐	DQSB0_7
(8)	-DQSB[0..7]	⇐	-DQSB0_7

LGA1155 (G,H,I)



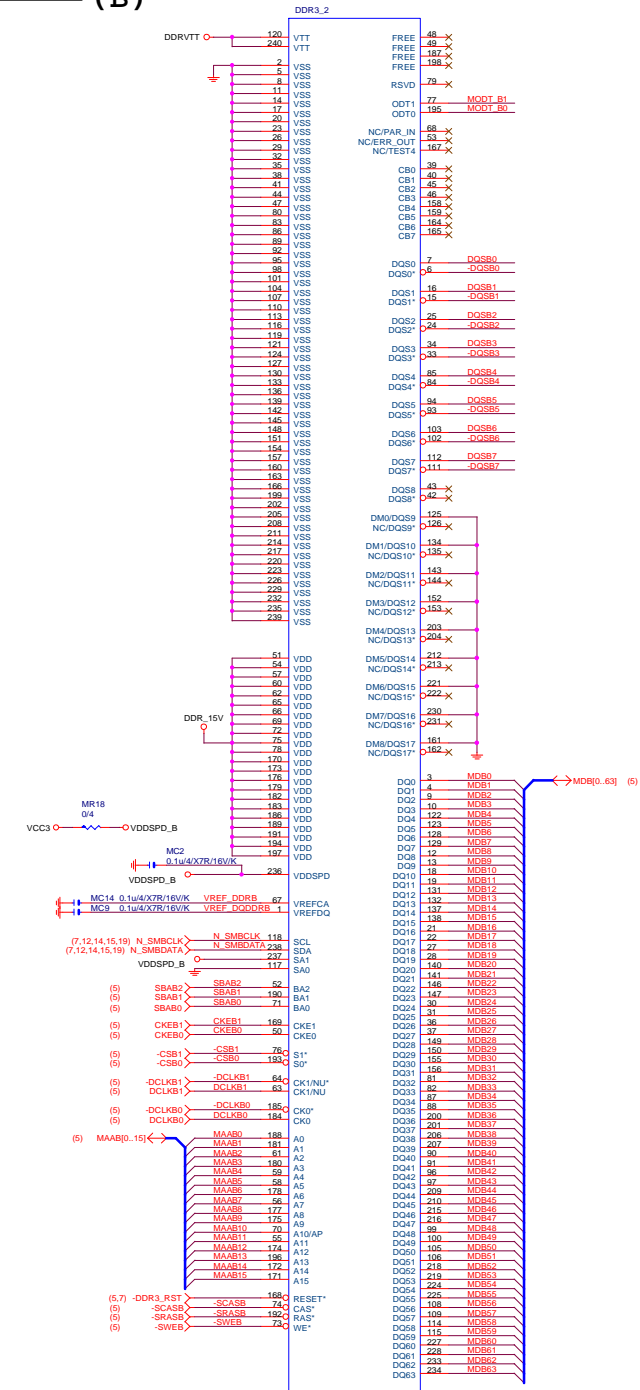
DDR CAP

(x9)

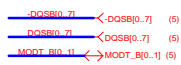


DDR3

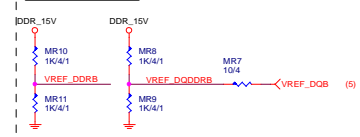
(B)



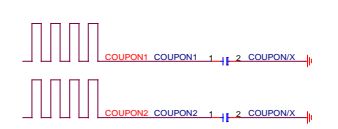
DDR3/240/BK/VA/D
BLACK CONNECTOR



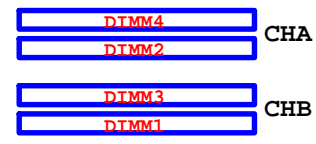
DDR3 VREF



COUPON



CPU



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DMI:12/4/4/4/12(breakout min 8/4/4/4/8)
Impedance=85 +- 17.5%

VCC1_5_PCH

NR50 7.5K/4/1 DMI_COMP B

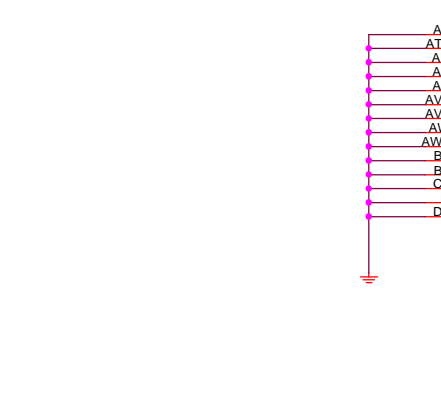
NR40 7.5K/4/1 PCIE_COMP C

CK -SRCCLK_PCH G

CK_SRCCLK_PCH F

放靠近 Device & PCI-E Slot
Impedance=80 +- 17.5%

PCIEX1:16/5/5/5/16 (breakout min 8/4/4/4/8)



USB2.0 : 12/4.5/7.5/4.5/12 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%

PCHB

B85: Port 6/7 N/A

H81: Port 6/7/12/13 N/A

DMI_RXN_0	USBN_0	AV10	N_-USBP0	N_-USBP0 (21)
DMI_RXP_0	USBN_0	AV10	N_-USBP0	N_-USBP0 (21)
DMI_TXN_0	USBN_1	AV11	N_-USBP1	N_-USBP1 (21)
DMI_TXP_0	USBN_1	AV11	N_-USBP1	N_-USBP1 (21)
DMI_RXN_1	USBN_2	AN14	N_-USBP1	N_-USBP1 (21)
DMI_RXP_1	USBN_2	AP14		
DMI_TXN_1	USBN_3	AK16		
DMI_TXP_1	USBN_3	AK16		
DMI_RXN_2	USBN_4	AV15	N_-USBP4	N_-USBP4 (18)
DMI_RXP_2	USBN_4	AV15	N_-USBP4	N_-USBP4 (18)
DMI_TXN_2	USBN_5	AV12	N_-USBP5	N_-USBP5 (18)
DMI_TXP_2	USBN_5	AV12	N_-USBP5	N_-USBP5 (18)
DMI_RXN_3	USBN_6	AV14		
DMI_RXP_3	USBN_6	AV14		
DMI_TXN_3	USBN_7	AV17	H81: Port 6/7/12/13 N	
DMI_TXP_3	USBN_7	AT17		
	USBN_8	AV16	N_-USBP8	N_-USBP8 (21)
DMI_RCOMP	USBN_8	AV16	N_-USBP8	N_-USBP8 (21)
PCIE_RCOMP	USBN_9	AN16	N_-USBP9	N_-USBP9 (21)
	USBN_9	AP16	N_-USBP9	N_-USBP9 (21)
CLKIN_DMI_N	USBN_10	AI18	N_-USBP10	N_-USBP10 (21)
CLKIN_DMI_P	USBN_10	AK18	N_-USBP10	N_-USBP10 (21)
	USBN_11	AP18	N_-USBP11	N_-USBP11 (21)
PCIE_PERN 1 USB3_RXN 2	USBN_11	AN18	N_-USBP11	N_-USBP11 (21)

PCIE_PERP_1_USB3_RXP	2	USBN_12	AW18	H81: Port 6/7/12/13 N
PCIE_PETN_1_USB3_TXN	2	USBP_12	AV18	
PCIE_PETP_1_USB3_TXP	2	USBN_13	AP20	
PCIE_PERN_2_USB3_RXN	3	USBP_13	AN20	

PCIE_PERP_2_USB3_RXN_3	OC0B_GP59	AF40	N_USBOC_F (1)
PCIE_PETN_2_USB3_TXN_3	OC1B_GP40	AF37	
PCIE_PETP_2_USB3_TXP_3	OC1B_GP40	AD39	N_USBOC_R (1)
PCIE_PERN_3	OC2B_GP41	AD30	
PCIE_PERP_3	OC3B_GP42	AF39	
PCIE_PETN_3	OC4B_GP43	AF39	
PCIE_PETP_3	OC5B_GP9	AC41	
PCIE_PERN_4	OC6B_GP10	AF40	
PCIE_PERP_4	OC7B_GP14	AG40	N_GPIO14

PCIE_PETN_4	USBRBIASB USBRBIAS	AV20	N_USBRBIAS	NR47	22.6/4/1
PCIE_PETP_4		AU20			
PCIE_PERN_5					
PCIE_PERP_5					
PCIE_PETN_5					
PCIE_PETP_5	CLKIN_DOT96N	AP11	CK -DOTCLK		
	CLKIN_DOT96P	AM11	CK_DOTCLK		

PCIE_PERN_6
PCIE_PERP_6
PCIE_PETN_6
PCIE_PETP_6
PCIE_PERN_7
PCIE_PERP_7
PCIE_PETN_7
PCIE_PETP_7
PCIE_PERN_8
PCIE_PERP_8
PCIE_PETN_8
PCIE_PETP_8

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NR130
8.2K/4

N_USBOC14

NR130
8.2K/4

0.3VDDUAL

N_USBOC_F

N_USBOC_R

NBC82
0.1u/4/X7R/16V/K

NBC83
0.1u/4/X7R/16V/K

H81/S

PCHF
USB3

(21)	PCH_USB3_RXN0	>	F20	USB3_
(21)	PCH_USB3_RXP0	>	G20	USB3_
(21)	PCH_USB3_TXN0	>	B18	USB3_
(21)	PCH_USB3_TXP0	>	C18	USB3_
(21)	PCH_USB3_RXN1	>	G18	USB3_
(21)	PCH_USB3_RXP1	>	H18	USB3_
(21)	PCH_USB3_TXN1	>	B15	USB3_
(21)	PCH_USB3_TXP1	>	B16	USB3_

N/A

- ~~K20~~ USB3
- ~~L20~~ USB3
- ~~D15~~ USB3
- ~~C15~~ USB3
- ~~L18~~ USB3
- ~~K18~~ USB3
- ~~B14~~ USB3
- ~~A14~~ USB3

VCC3

NR62 8.2K/4 AK28

NR63 8.2K/4 AT34

TACH+ TACH-

H81/S

F

F

CK_SRCCLK_PCH

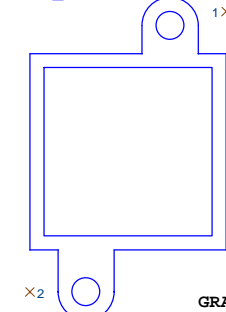
CK_-SRCCLK_PCH

Mount for integrated

CK_DOTCLK	N
CK_DOTCLK	N
NR225 short to GN	
graphic SKU	

LOW COST ICH7 HEATSINK

SB_HEATSIN



PCH_HS
PCH_HS/[12SP2-030005-41R]

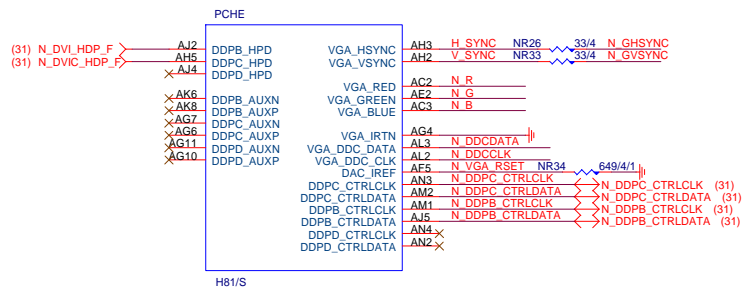
```
OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)
```

USB OC#	Configure
OC0#	F_USB30
OC1#	USB_LAN
OC2#	R_USB30
OC3#	N/A
OC4#	F_USB1
OC5#	F_USB2
OC6#	KB_MS_USB
OC7#	Not Use

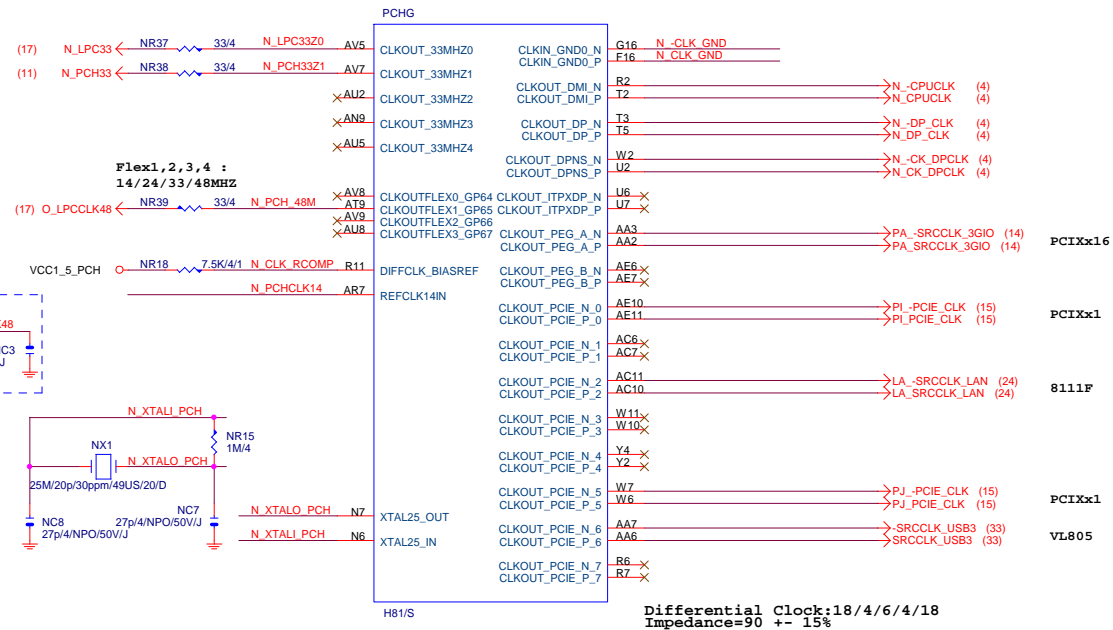
Gigabyte Technology

Title			
PCH FDI,DMI,USB ,PCIE,NVRAM			
Size	Document Number		Rev
Custom	GA-H81M-D2W WG		1.0
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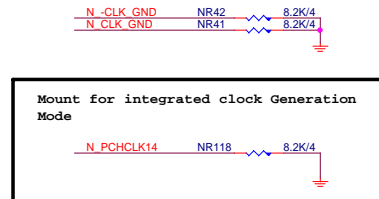
PCH (E)



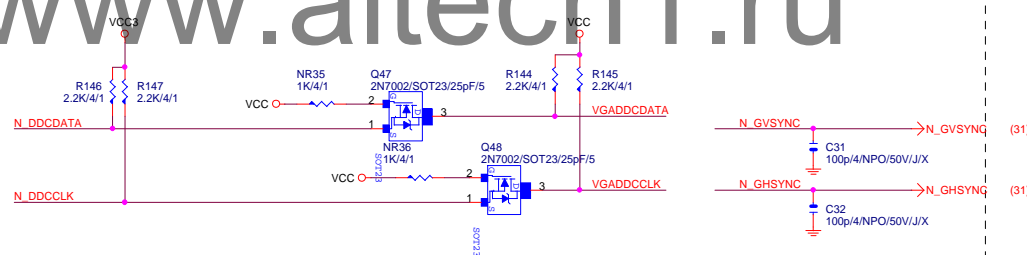
PCH (G)



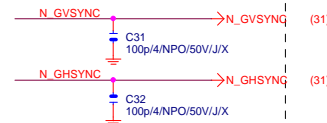
PCH CLK PD



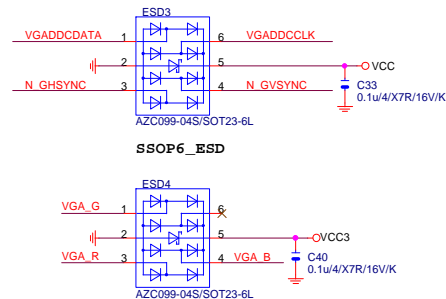
VGA DDC



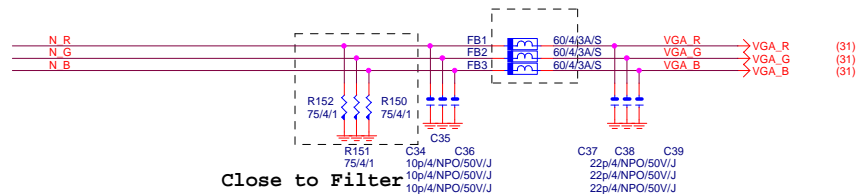
VGA CONNECTOR



VGA ESD

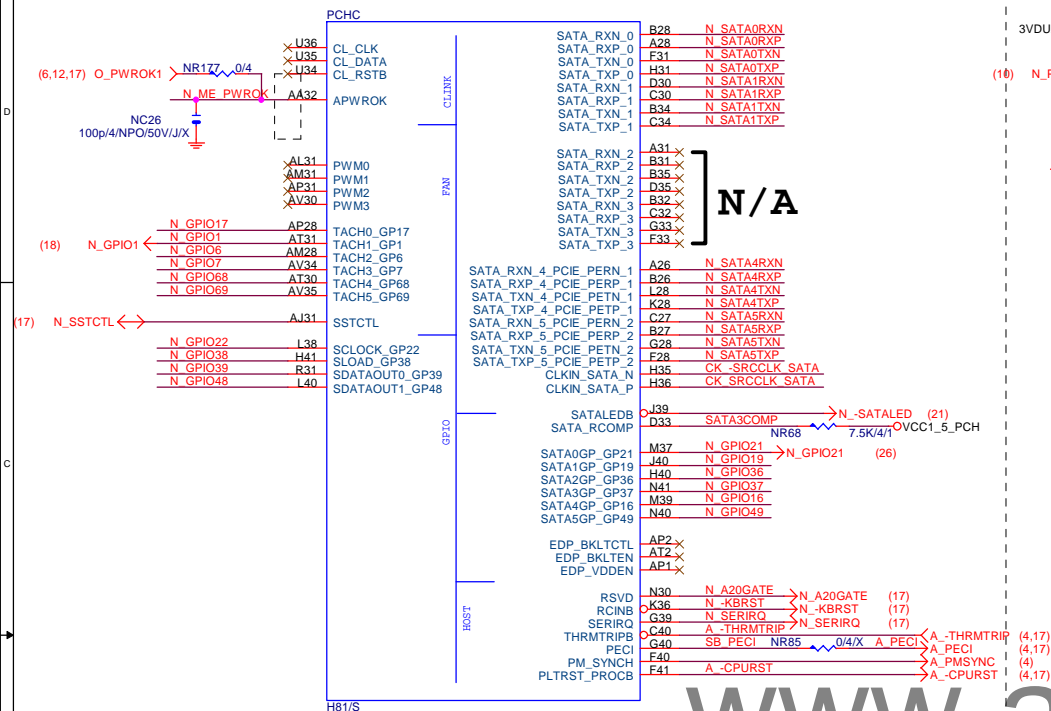


VGA DDC

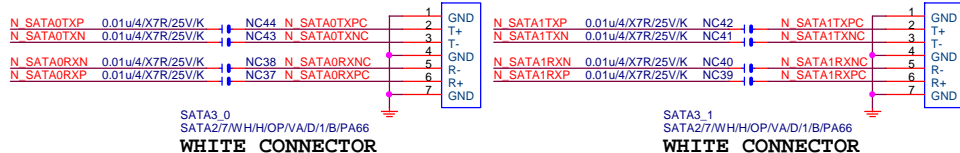


(C)

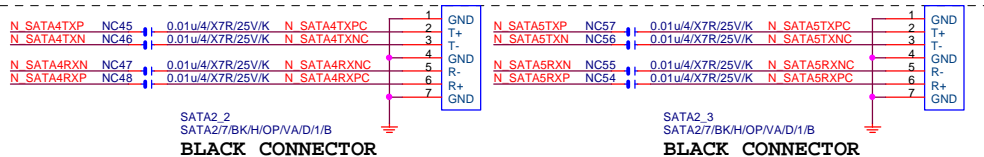
SATA3 : 20/7.5/4.5/7.5/20 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%
SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%



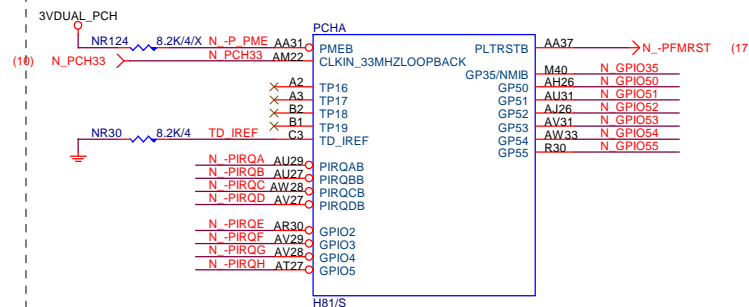
SATA CONNECTOR



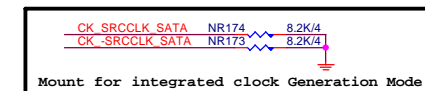
```
** Z87/H87 Port 4&5 SATA3.0
** B85 Port 4&5 SATA2.0
```



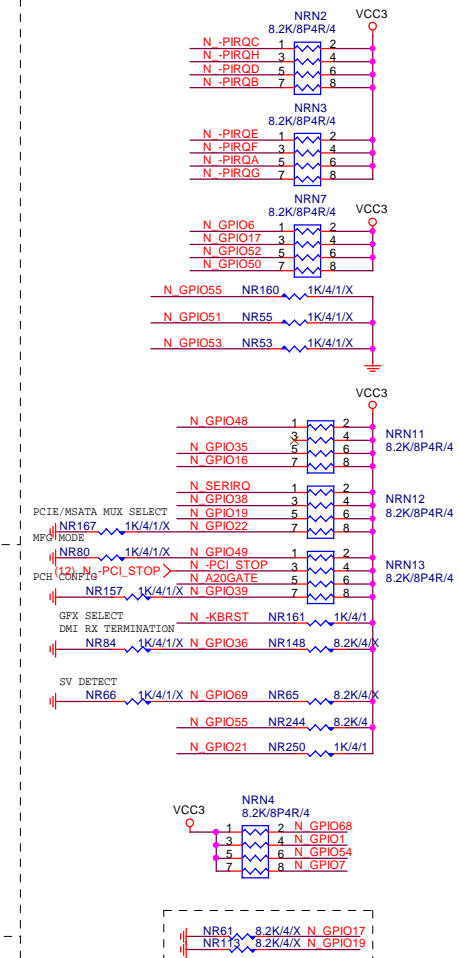
(A)



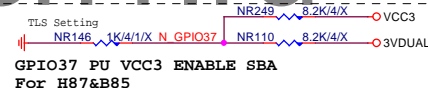
PCH	CLK	PD
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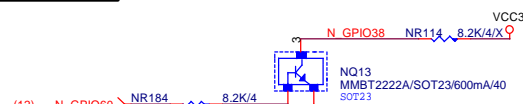
PCH	PU/PD
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ME PWROK



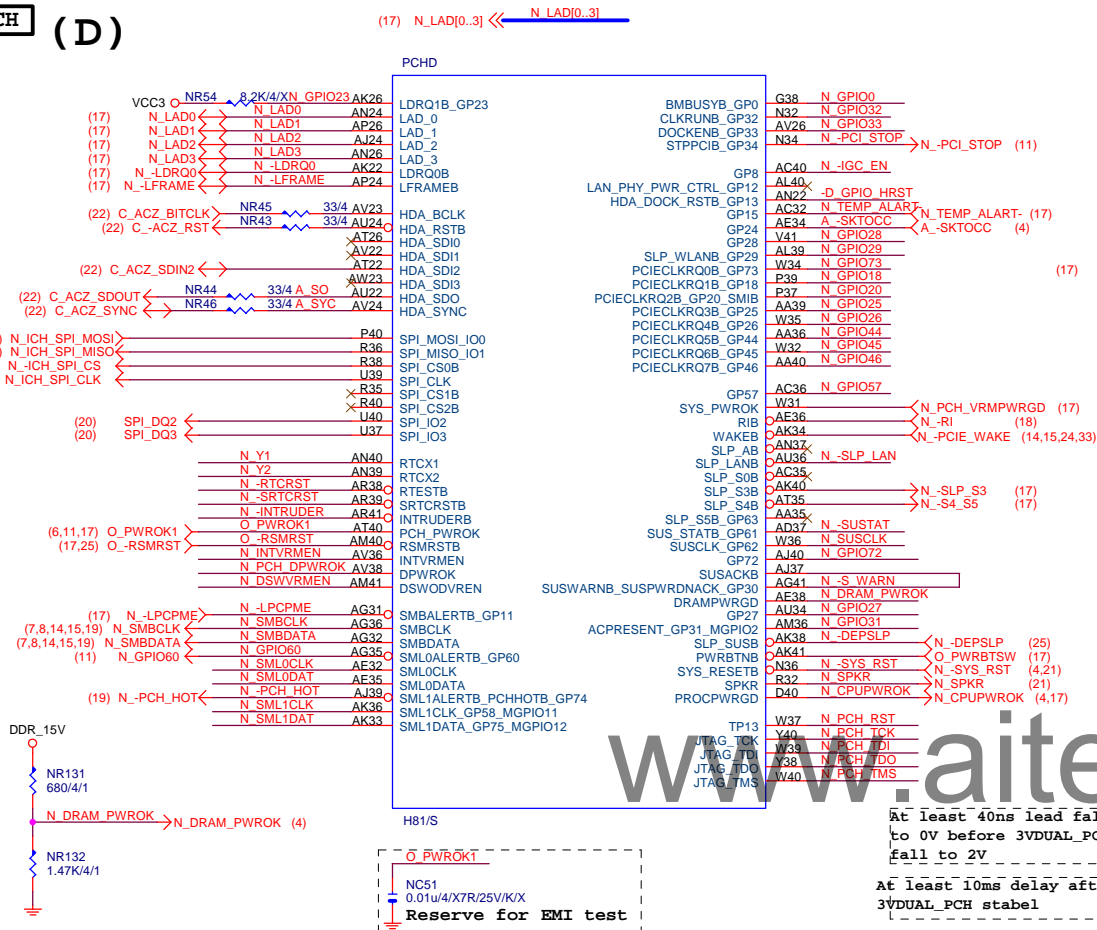
GPIO38 Ctrl



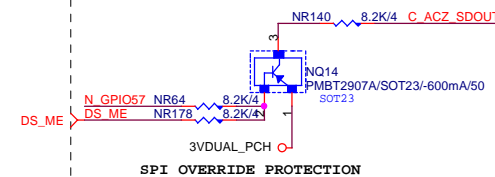
Gigabyte Technology

Title			
PCH HOST , SATA, PCI			
Size	Document Number		Rev
Custom	GA-H81M-D2W WG		1.0
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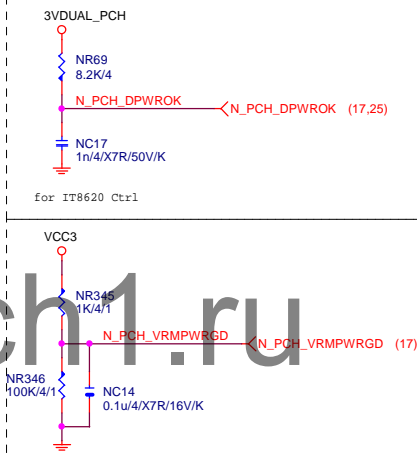
(D)



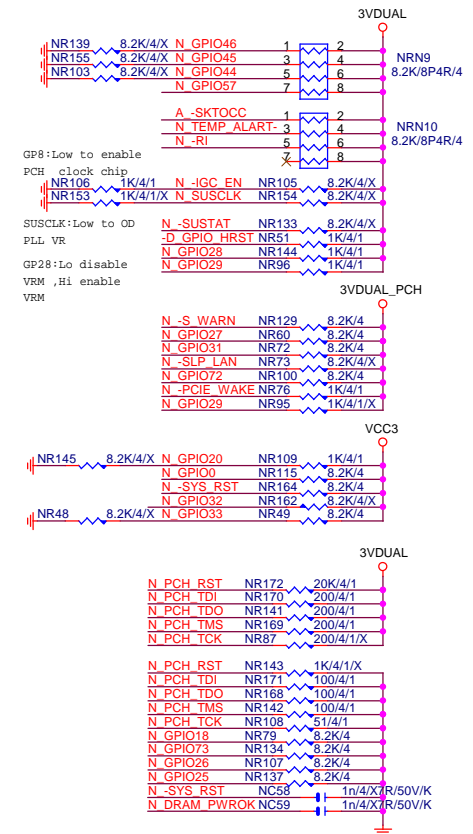
ACZ_SDOUT



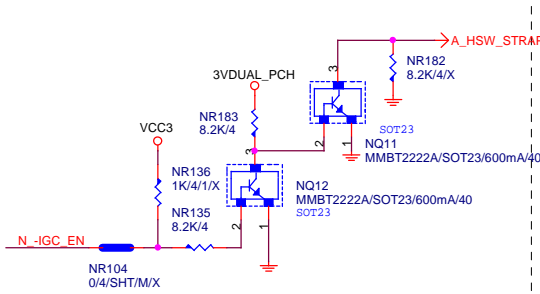
PCH_DPWROK



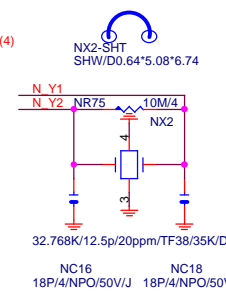
PCH	PU/PD
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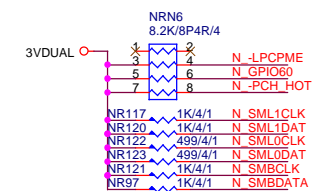
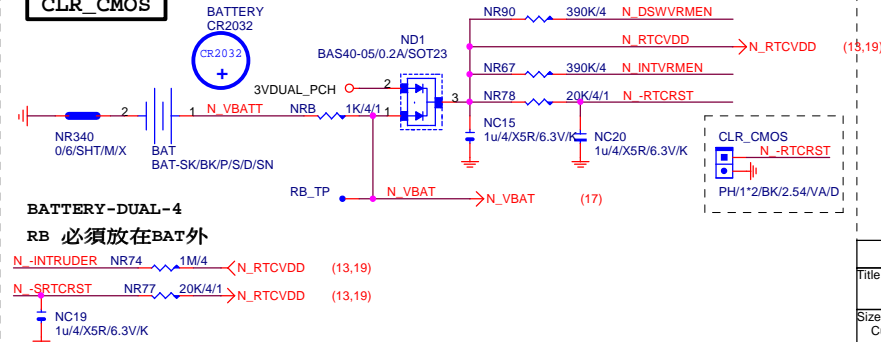
HSW STRAP13



32.768KHZ



CLR	CMOS
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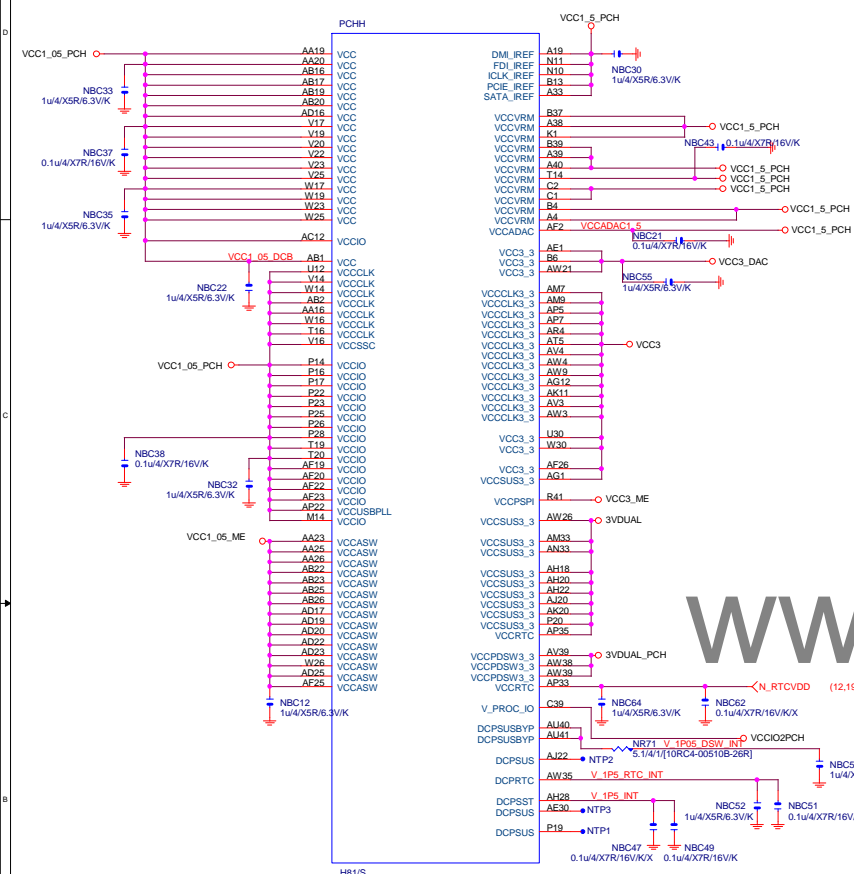
Gigabyte Technology

PCH GPIO , CTRL , AUDIO

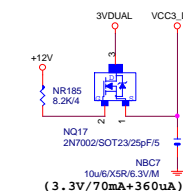
GA-H81M-D2W WG

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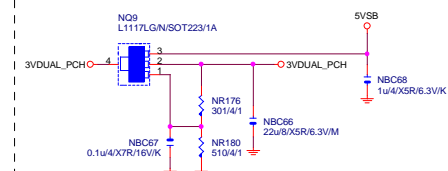
PCH (H)



VCC3_DAC



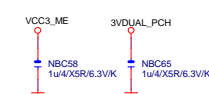
3VDUAL_PCH



SHT PWR

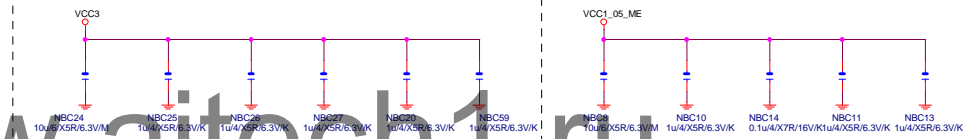


CAP

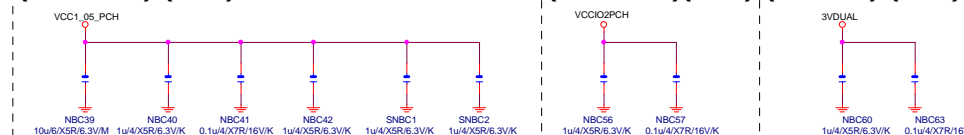


(3.3V) (X6)

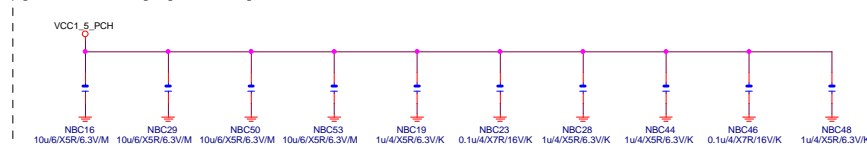
(1.05V) (x5)



(1.05V)(x6)

$$(1.05V)(x_2) - (3.3V)(x_2)$$


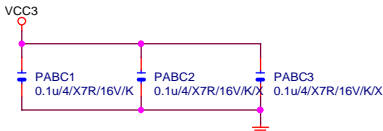
(1.05V) (x10)



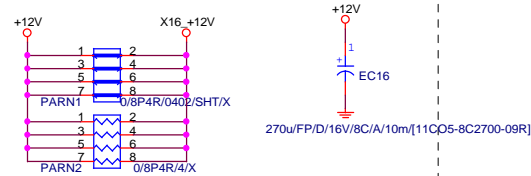
PCH (I)



PCIEX16 CAP



PCIEX16 PROTECT SHT



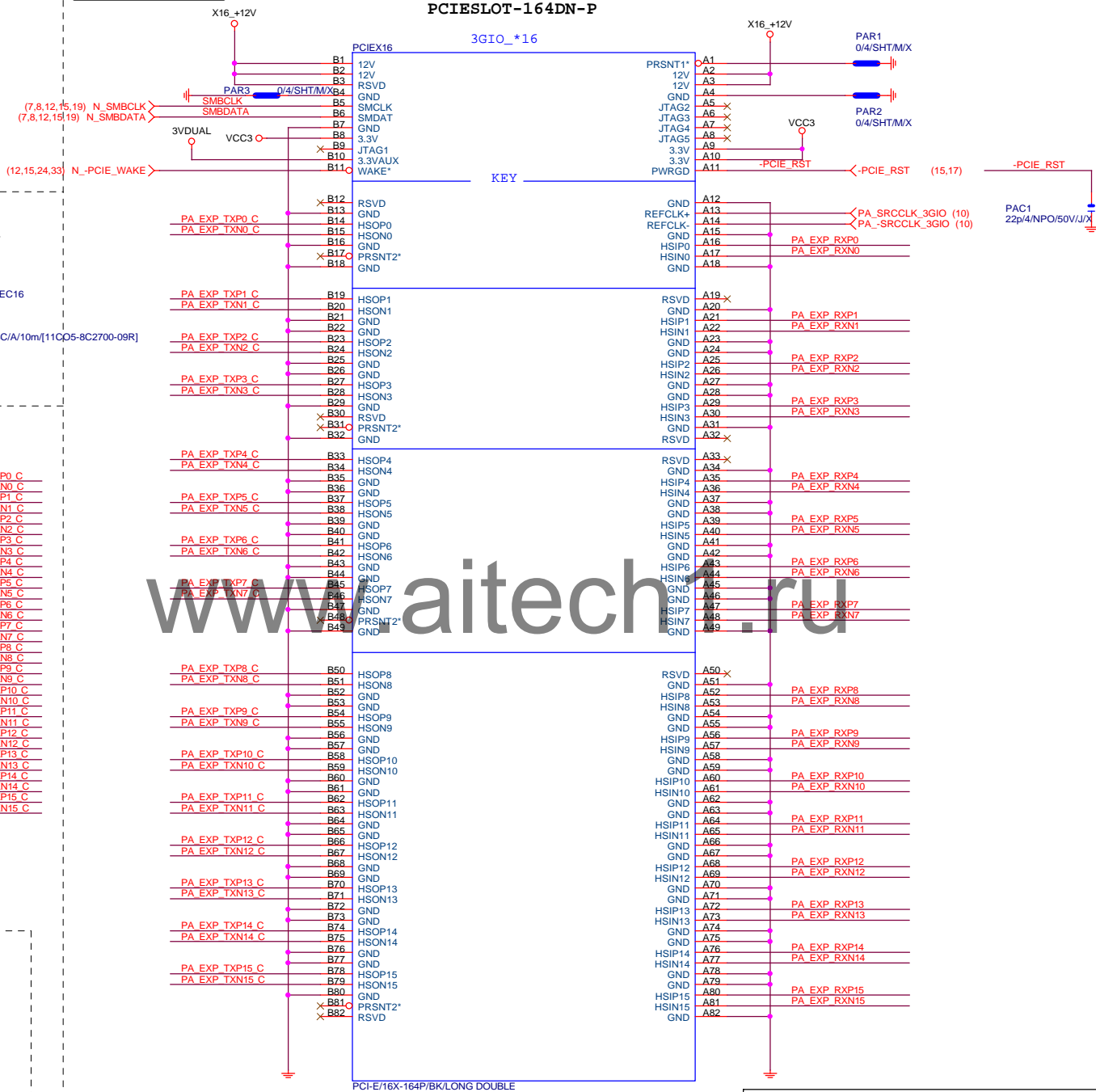
PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXP0.[15] >>> PA_EXP_RXP[0..15] (4)
PA EXP RXN0.[15] >>> PA_EXP_RXN[0..15] (4)
PA EXP TXP0.[15] >>> PA_EXP_TXP[0..15] (4)
PA EXP TXN0.[15] >>> PA_EXP_TXN[0..15] (4)

The auxiliary reset circuit is only required for PCIe Gen3 margining and functional link training

PCIEX16 SLOT



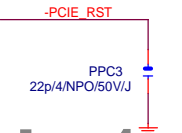
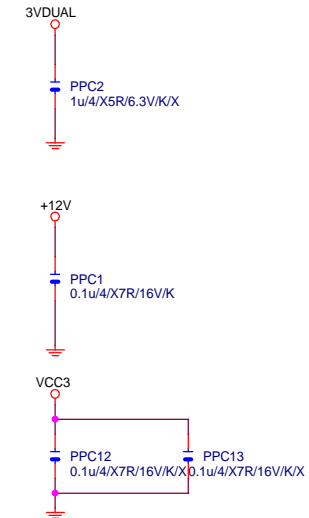
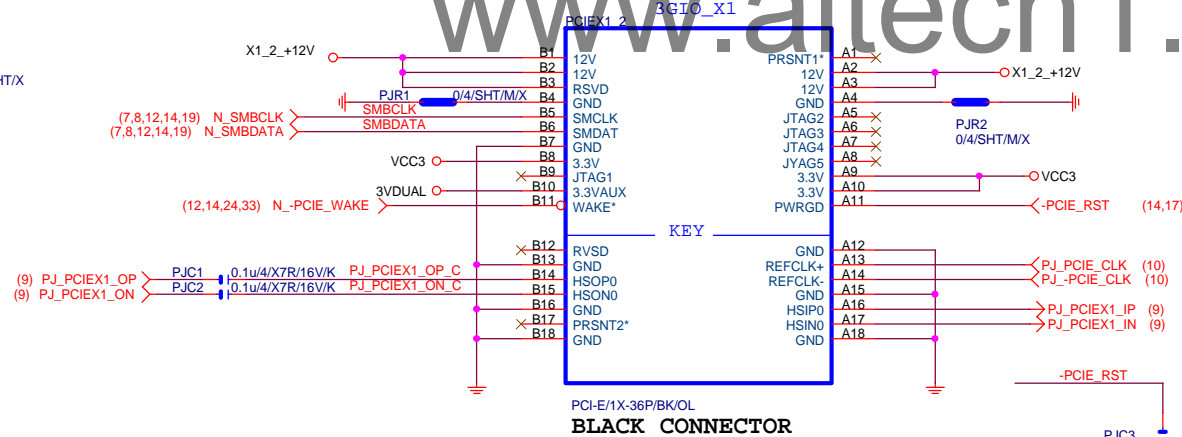
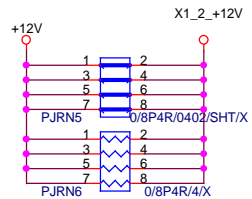
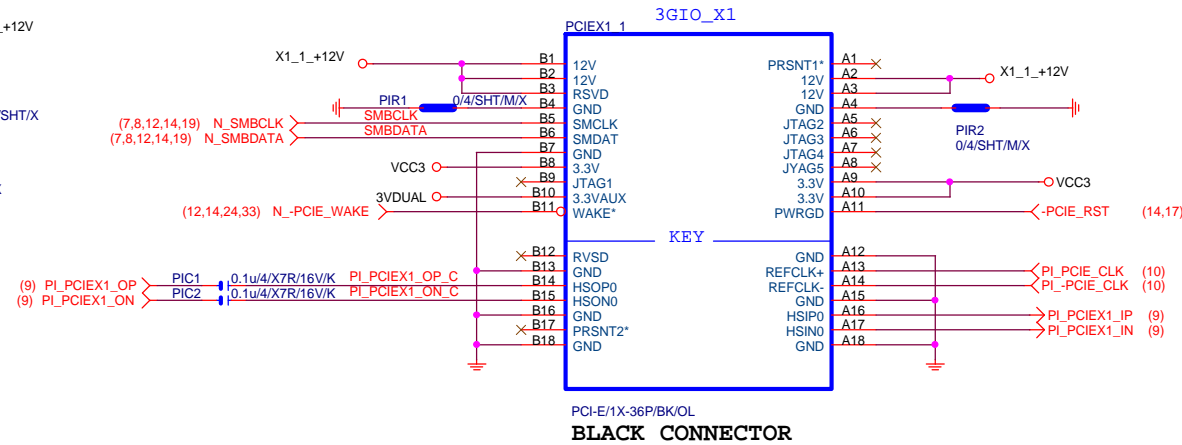
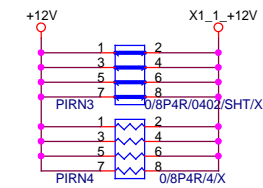
PCI-E/16X-164P/BK/LONG DOUBLE

BLACK CONNECTOR

Gigabyte Technology

Title			PCI EXPRESS * 16	
Size			GA-H81M-D2W WG	
Custom			Rev 1.0	
Date:			Thursday, September 05, 2013	Sheet 14 of 33

PCIEX1 SLOT



Gigabyte Technology

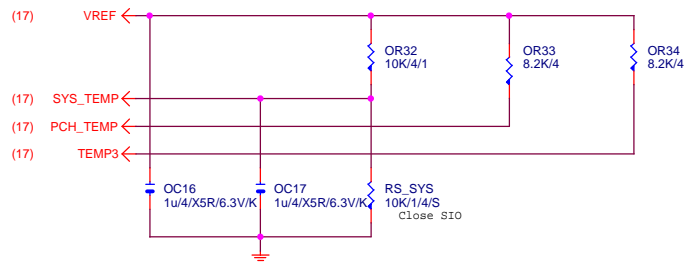
PCI EXPRESS X 1 PORT

Title	Document Number	Rev
Size Custom	GA-H81M-D2W WG	1.0
Date:	Thursday, September 05, 2013	Sheet 15 of 33

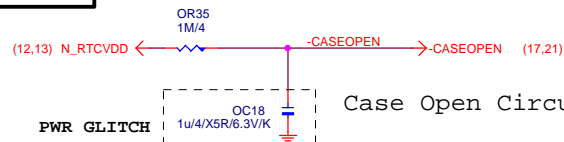
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Gigabyte Technology			
Title			
PCI SLOT 1&2			
Size	Document Number		Rev
Custom	GA-H81M-D2W WG		1.0
Date:	Thursday, September 05, 2013	Sheet	16 of 33

TEMP H/W MONITOR

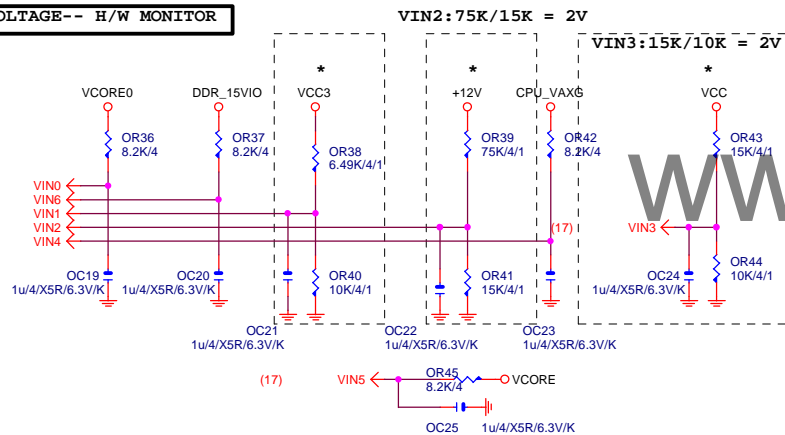


CASE OPEN



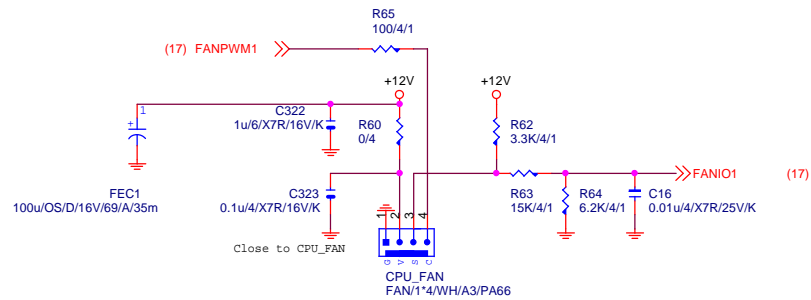
Case Open Circuits

VOLTAGE-- H/W MONITOR

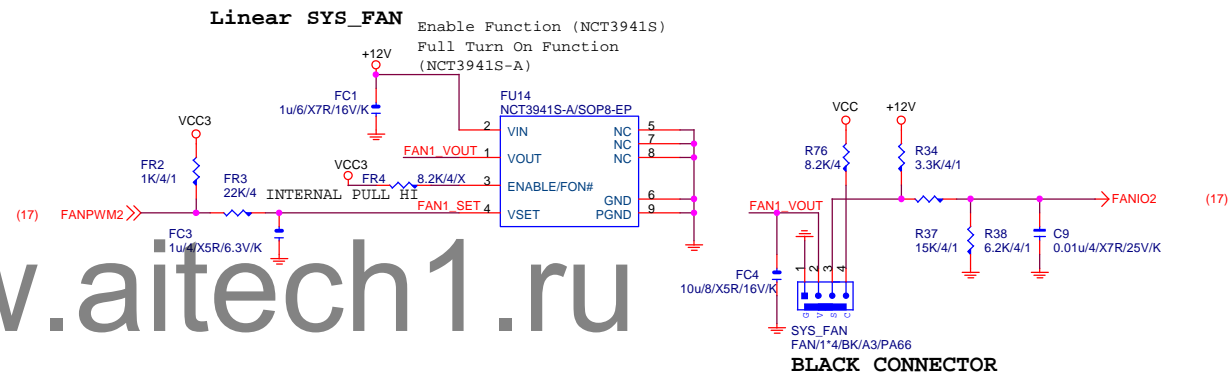


RS1、RS2、RS3 CLOSE CPU VR MOSFET

CPU SMART FAN

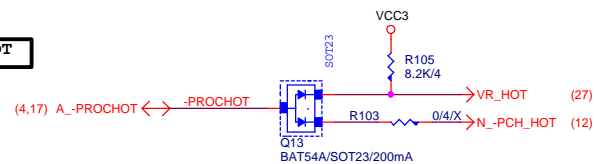


SYS SMART FAN



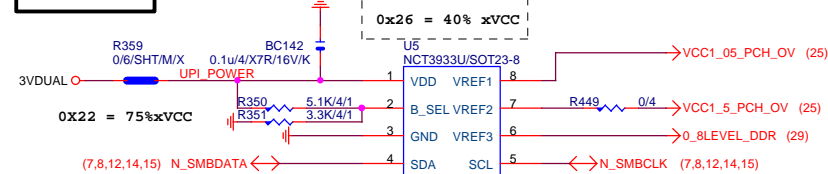
BLACK CONNECTOR

-PROHOT



接pwm feedback pin

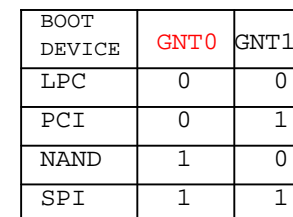
OV NCT3933



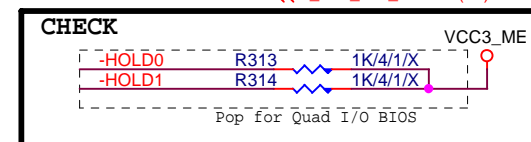
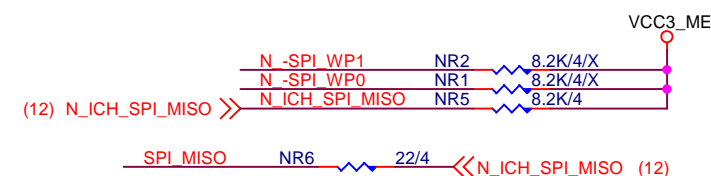
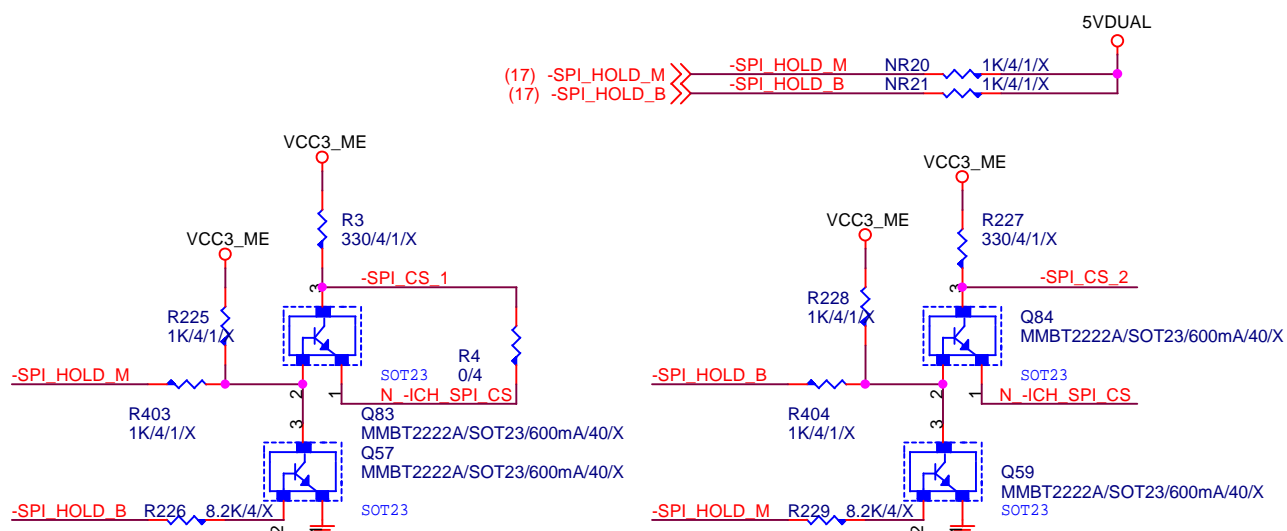
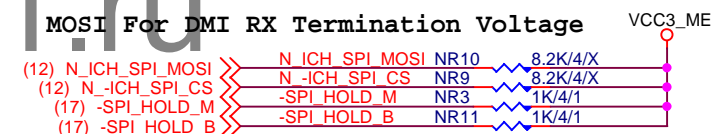
NCT3933	0X2A	0X20	0X22
VREF1	DDRVTT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

Gigabyte Technology

Title			HWM,FAN CTRL,OV
Size	Document Number	Rev	
Custom	GA-H81M-D2W WG	1.0	
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MOSI For DMI RX Termination Voltage



DUAL BIOS

GA-H81M-D2W WG

Rev	1.0
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Title

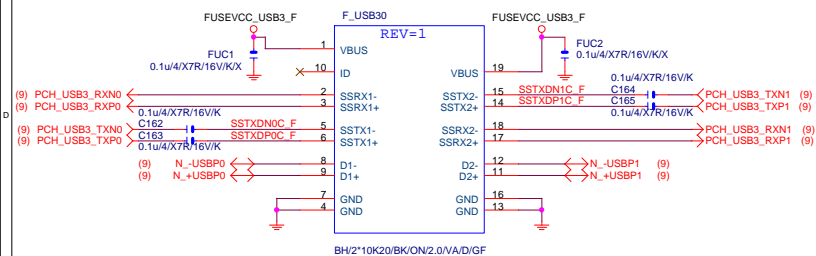
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Document Number

Date: Thursday, September 05, 2013

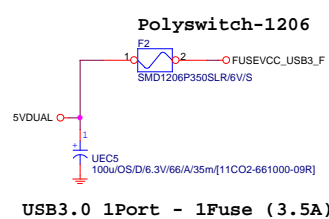
Sheet 20 of 33

F_USB30

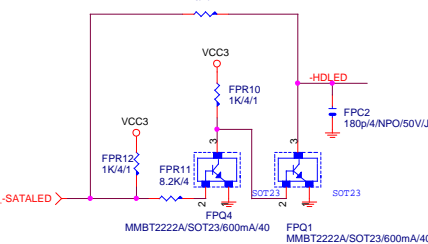


BLACK CONNECTOR

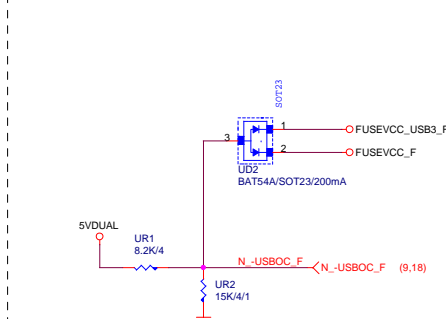
F_USB30 PWR



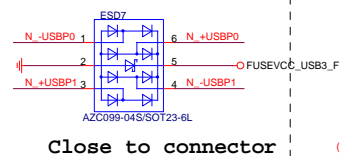
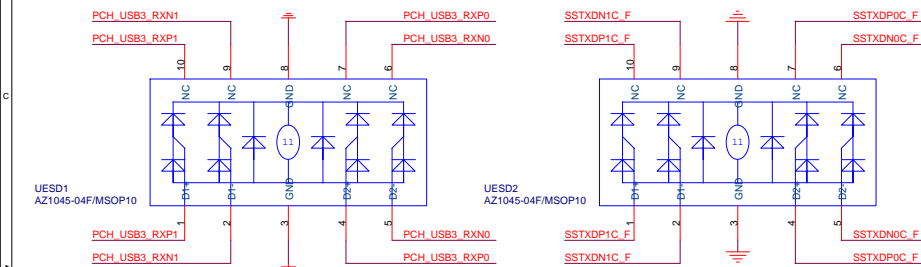
SATA LED



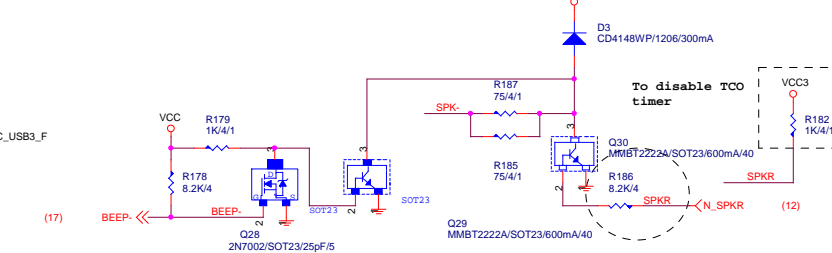
-USB0C_F



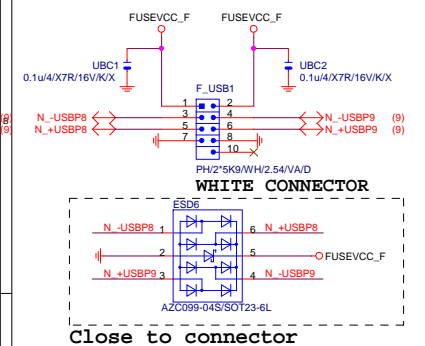
F_USB30 ESD PROTECT



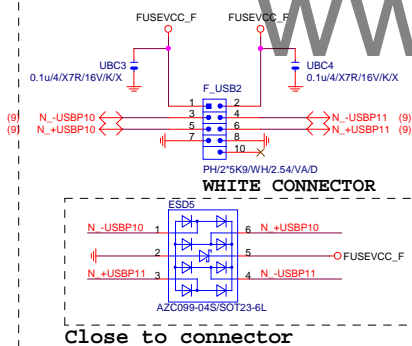
SPKR



FRONT USB1

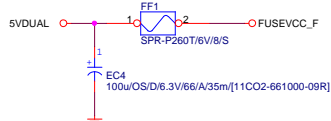


FRONT USB2

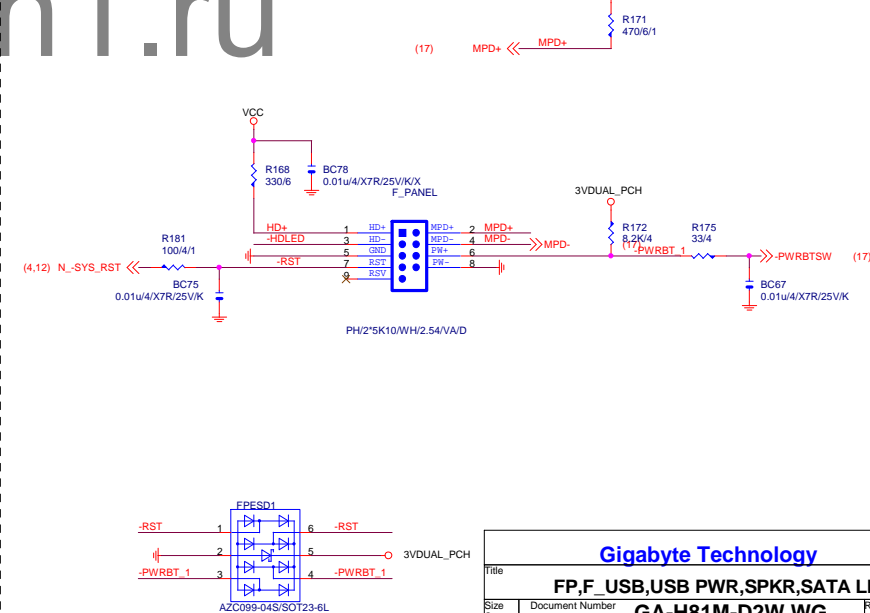


FUSE-0805

F_USB1, F_USB2 4-Port 2.6A



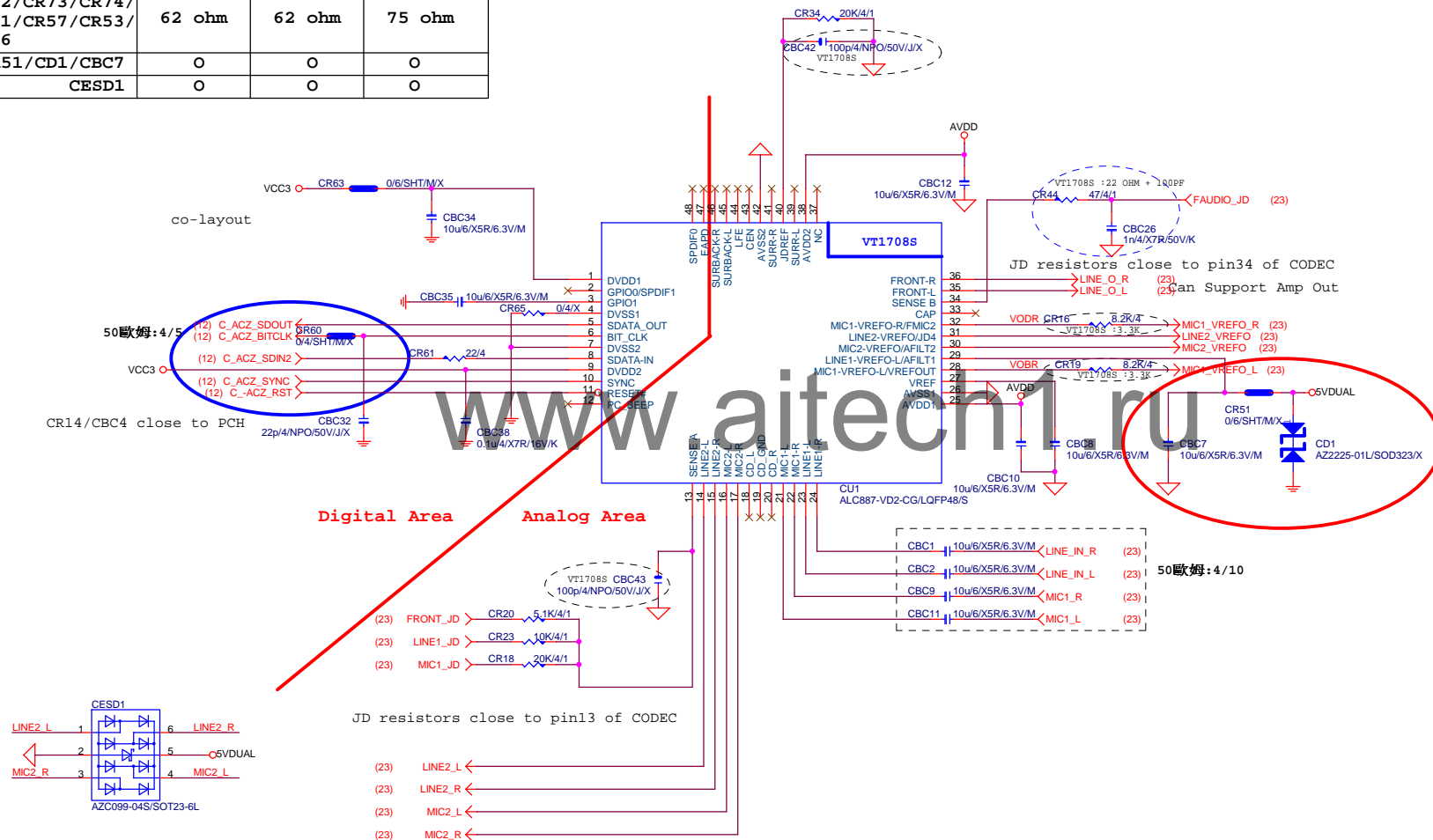
INTEL FRONT PANEL

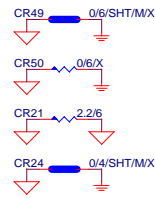


Gigabyte Technology			
Title	FP,F_USB,USB PWR,SPKR,SATA LED		
Size	Document Number	GA-H81M-D2W WG	
Date	Thursday, September 05, 2013	Sheet	21 of 33

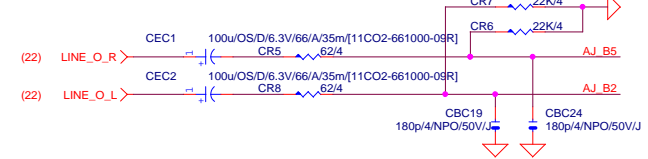
AZALIA CODEC ALC892/ALC887-VD2/VT1708-CE Colay

	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O





LINE-OUT

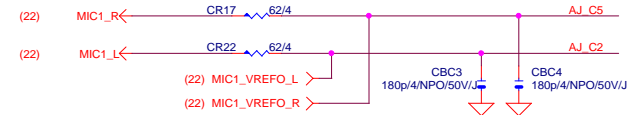


LINE-IN

Verify MIC function
in LINE-in

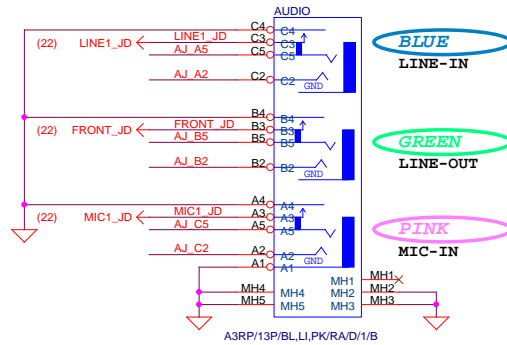
For 889A/888

MIC-IN

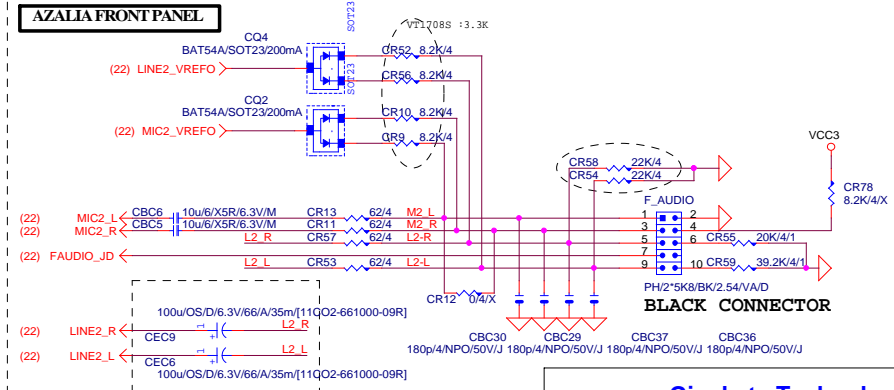


SPDIF_OUT

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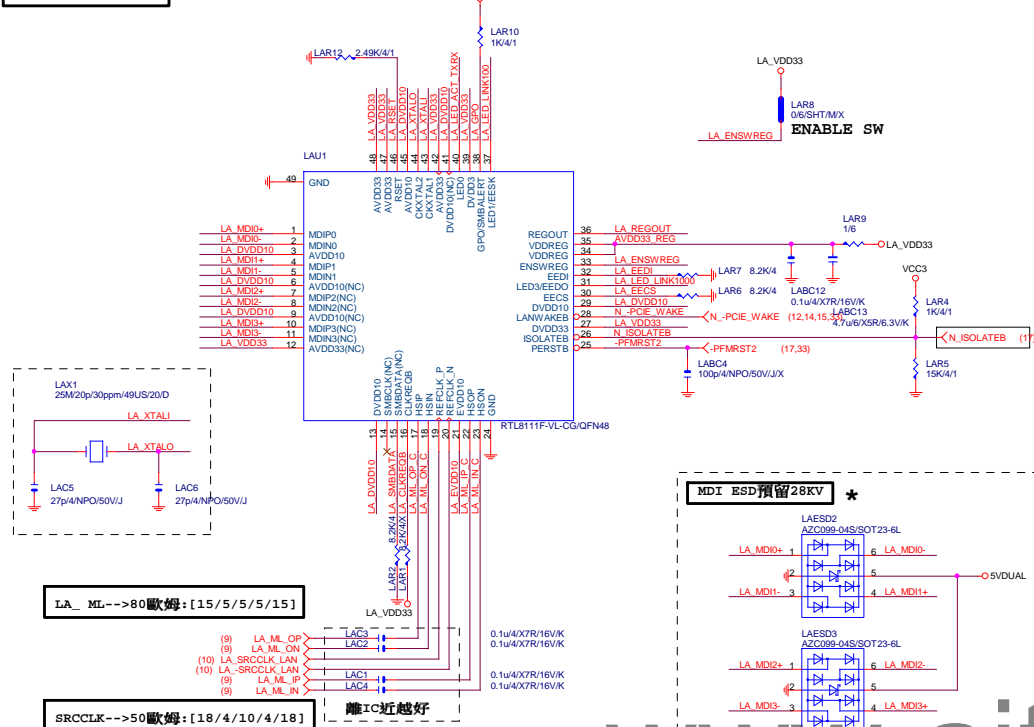
AZALIA FRONT PANEL



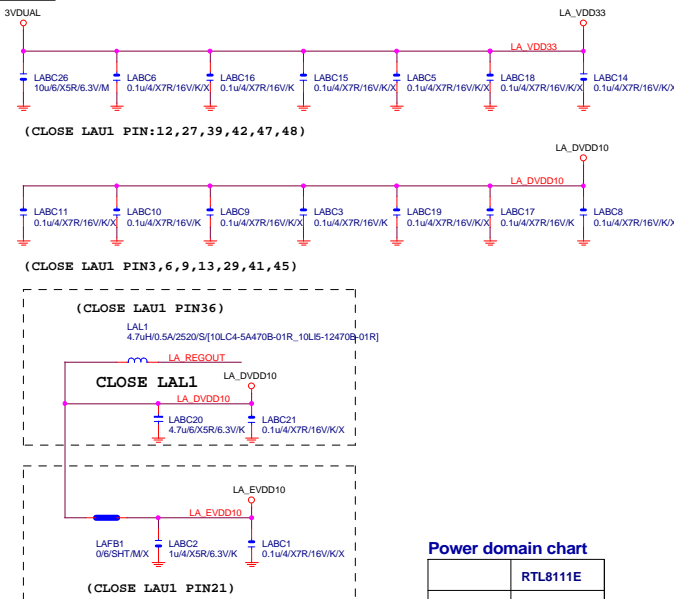
Gigabyte Technology

Title			
AUDIO JACK			
Size	Document Number	GA-H81M-D2W WG	
Custom			Rev 1.0
Date:	Thursday, September 05, 2013	Sheet	23 of 33

LAN:RTL8111F/VB/VL



LAN POWER

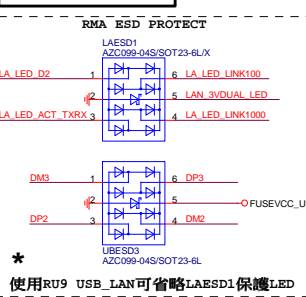


Power domain chart

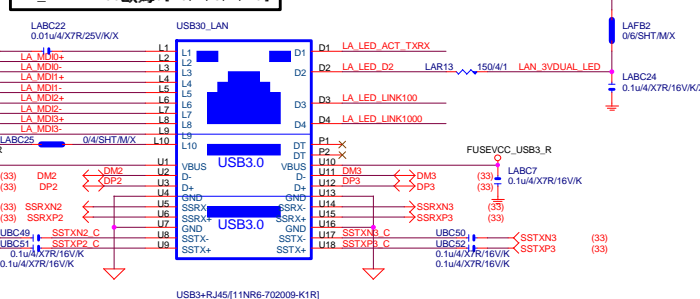
	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

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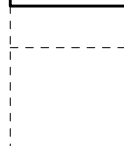
USB LAN CONNECTOR



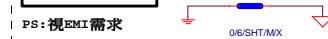
LA_MDI-->100歐姆:[20/4/8/4/20]



USB X3 POWER

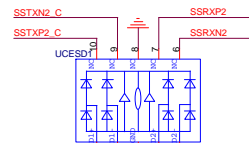
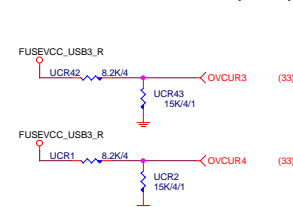


EMI SHORT PAD

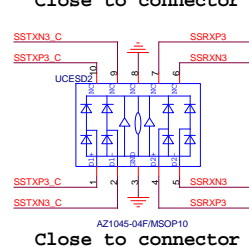


Polyswitch-1206

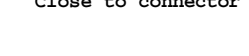
USB3.0 1Port - 1Fuse (3.5A)



Close to connector

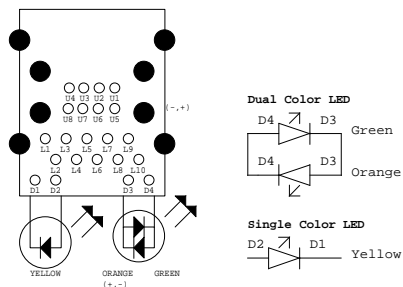


Close to connector



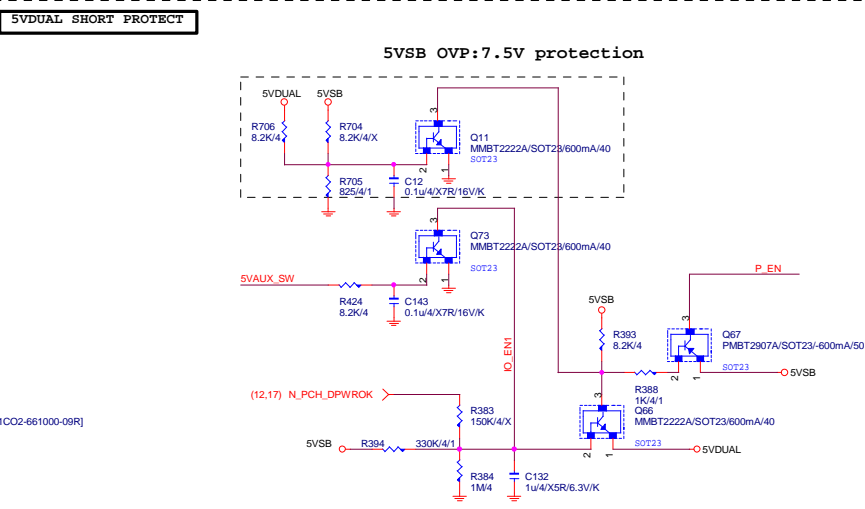
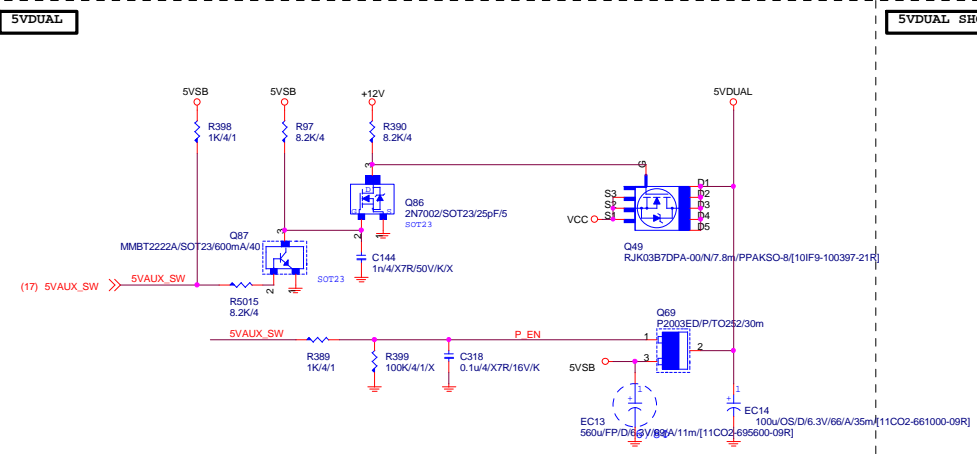
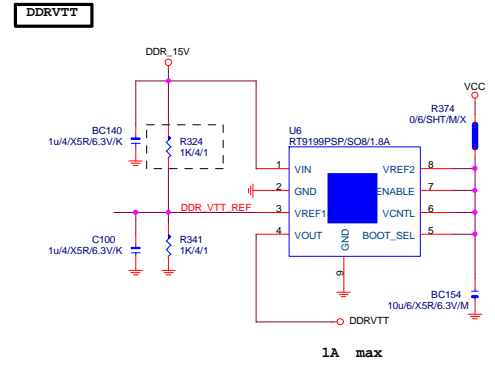
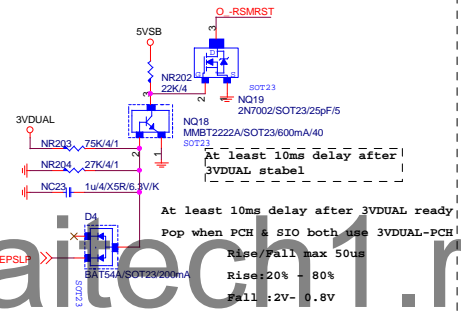
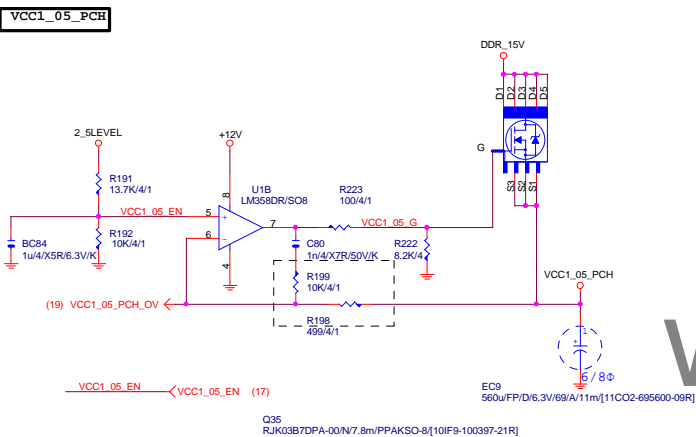
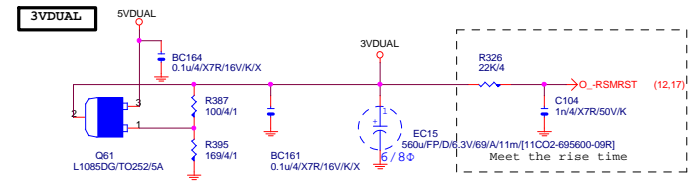
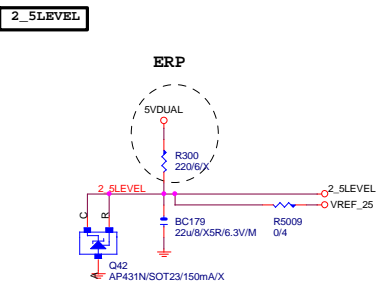
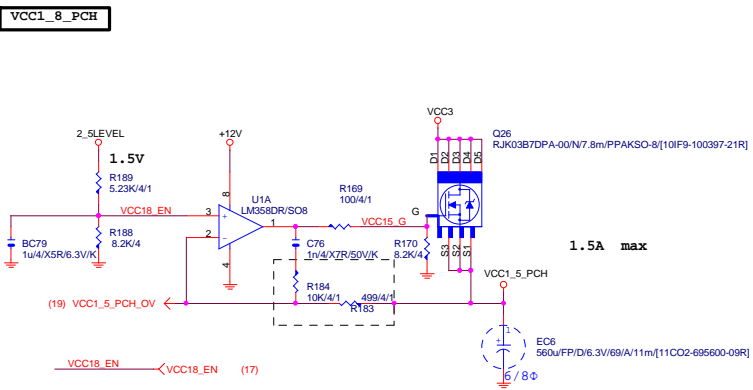
Gigabyte Technology

Title	Realtek RTL8111G	Rev	1.0
Size	Document Number	GA-H81M-D2W WG	
Custom			
Date	Thursday, September 05, 2013	Sheet	24 of 33



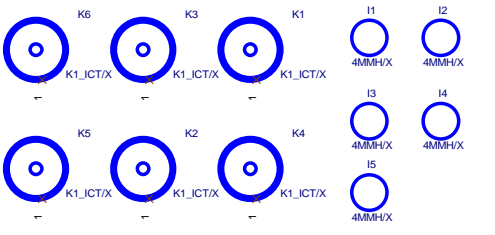
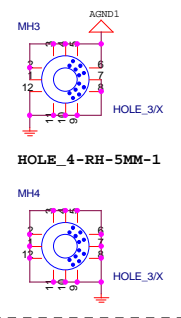
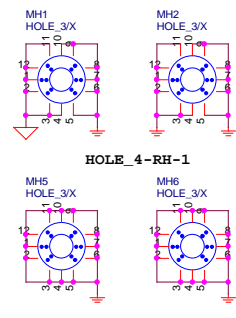
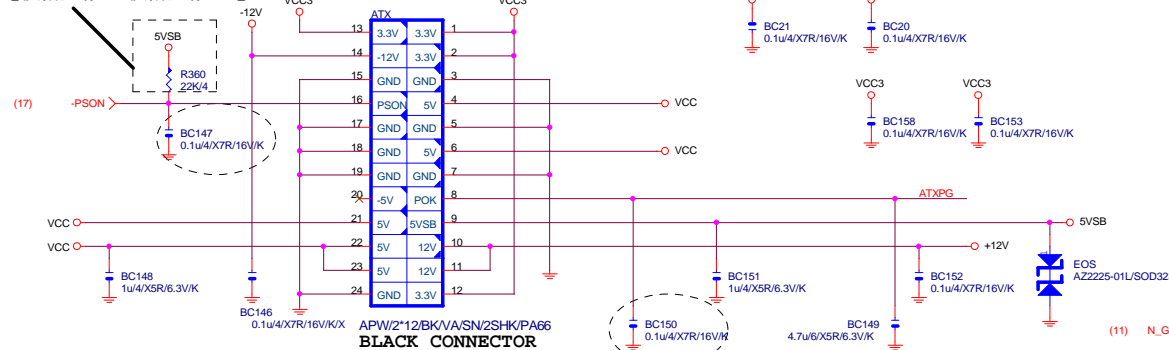
BOM NOTICE

料號	規格	廠商
11NR6-702009-96R	1G LAN (12core)	UDE(RU9 ESD+)
[LED獨立走線, 可省略外加AZC099料件LAESD1]		
1.	9KV ESD BOM:	
	USB LAN (RU9):11NR6-702009-96R	
2.	28KV ESD BOM:	
	USB LAN (RU9):11NR6-702009-96R	
	LAESD2, LAESD3: 上件AZC398-04S	



ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】

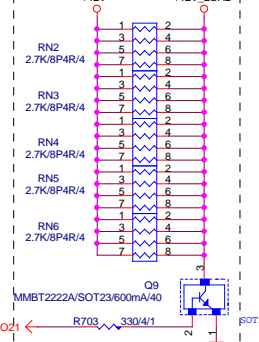


To prevent the 5VSB under loading when boot

TPM

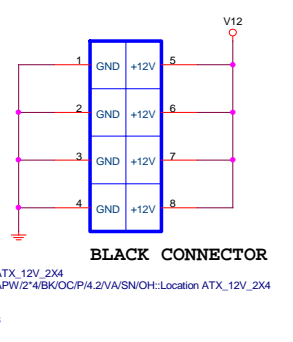
【技術通報R&D技術通報158】

To fix 12V light load abnormality issue



ATXX4 POWER CONNECTOR

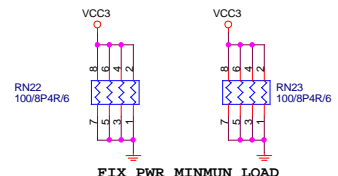
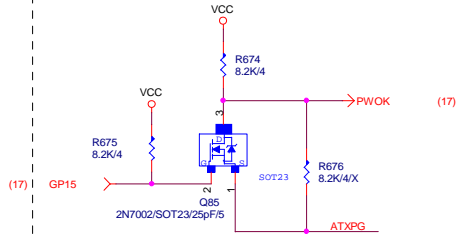
To fix 12V light load abnormality issue



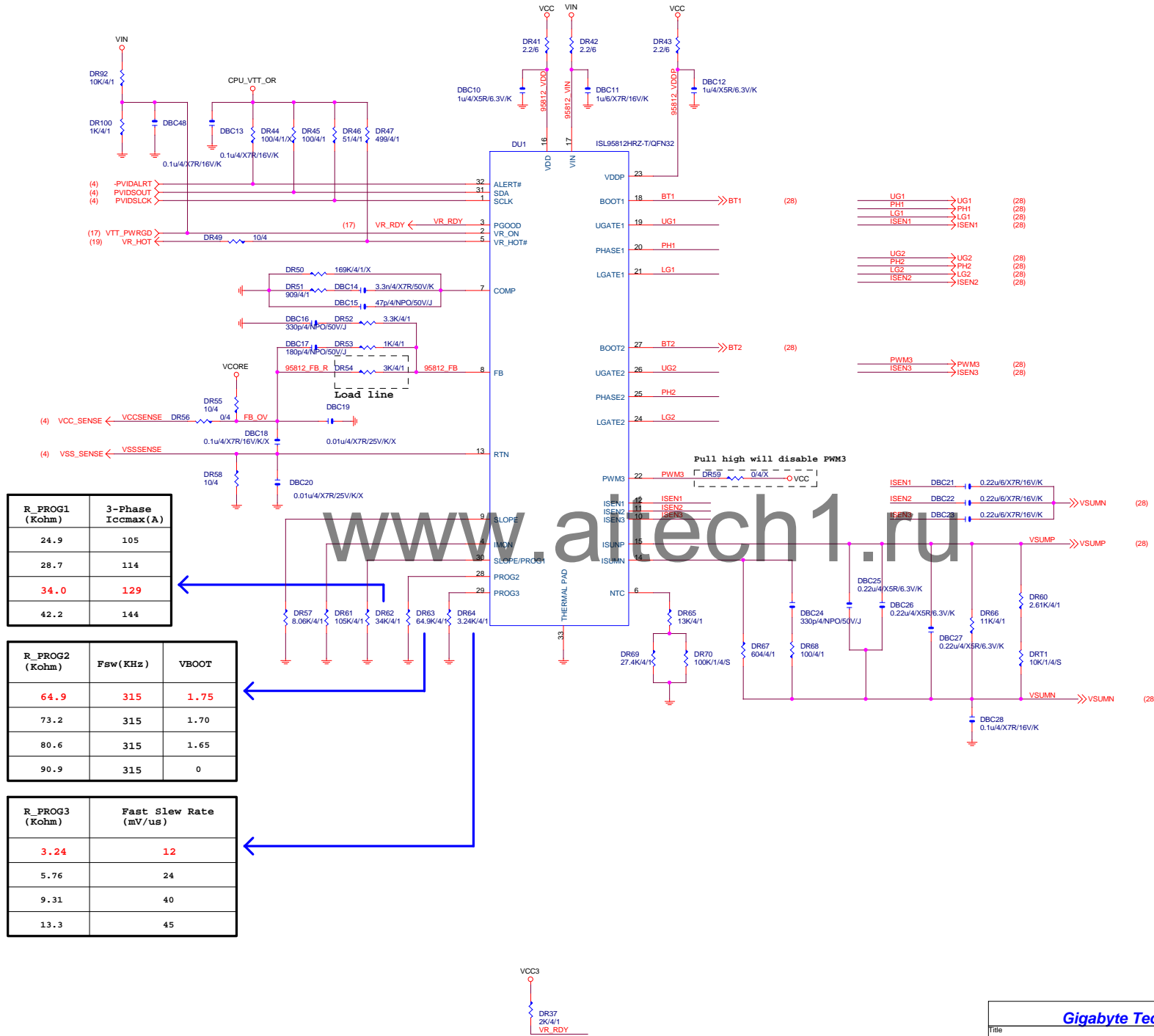
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PWOK PATCH

【技術通報R&D技術通報154】



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ATX CONNECTOR		
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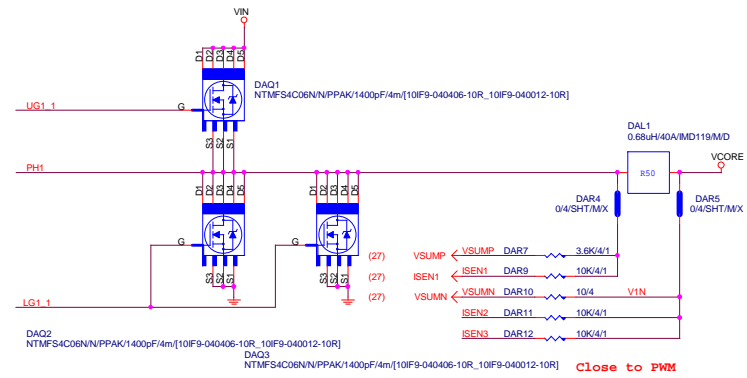
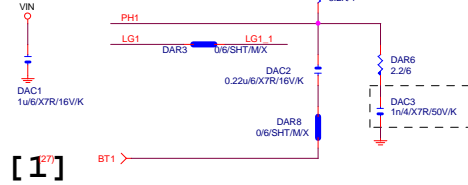
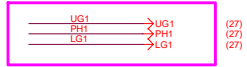


R_PROG1 (Kohm)	3-Phase Iccmax(A)
24.9	105
28.7	114
34.0	129
42.2	144

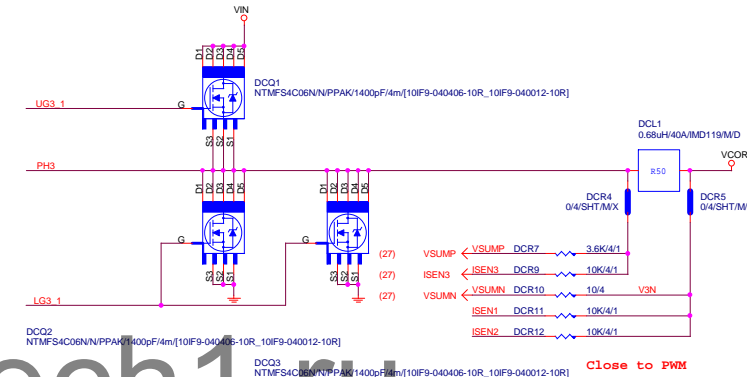
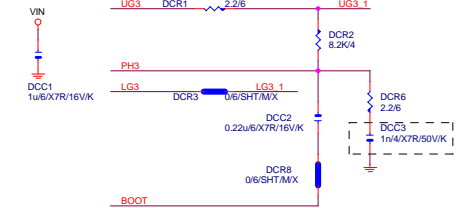
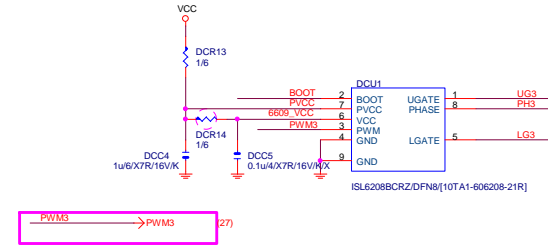
R_PROG2 (Kohm)	Fsw(KHz)	VBOOT
64.9	315	1.75
73.2	315	1.70
80.6	315	1.65
90.9	315	0

R_PROG3 (Kohm)	Fast Slew Rate (mV/us)
3.24	12
5.76	24
9.31	40
13.3	45

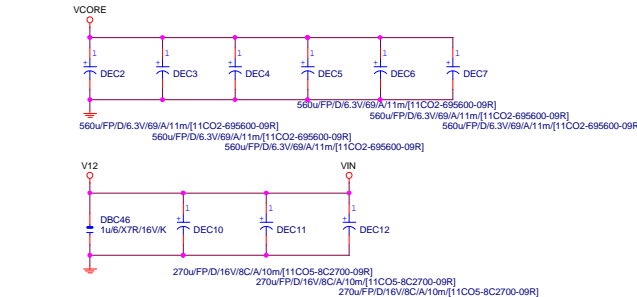
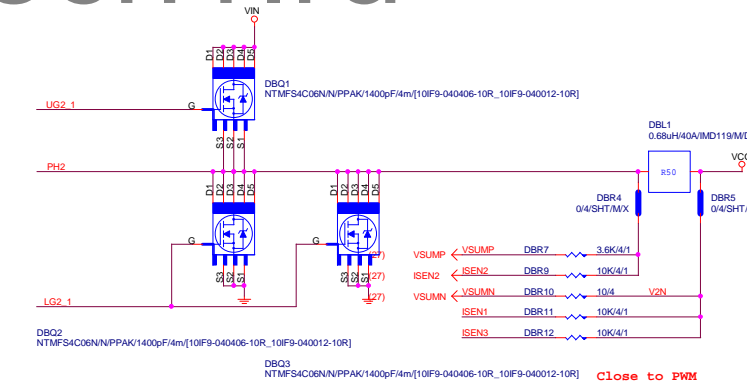
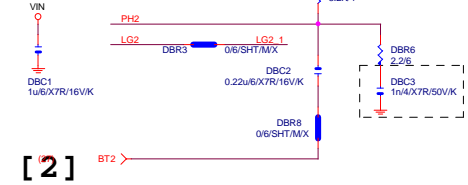
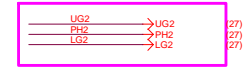
PHASE 1



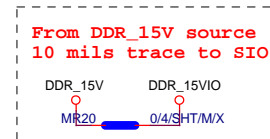
PHASE 3




PHASE 2



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VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
IRMS=11.45A
560uF/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85°C), 1(105°C)
VIN Ripple current=4.7X1.7=7.99A(85°C)
-->故固態電容須2X7.99=15.98>11.45A

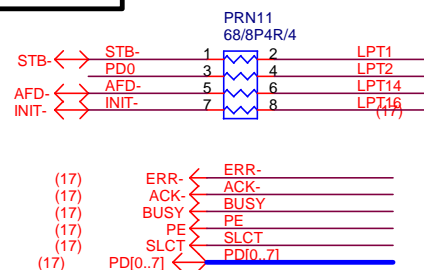
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Title			
DDR POWER			
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VCC1_05_ME

【技術通報R&D技術通報156】
(RICHTEK), (NUVOTON), (EMC)做共用
PIN7分壓阻值須做修改為100K以上電阻值

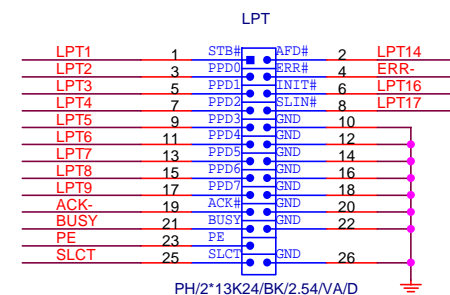
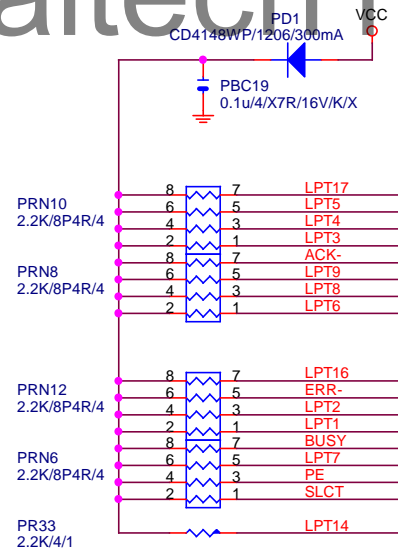
VCC3_ME

LPT PORT



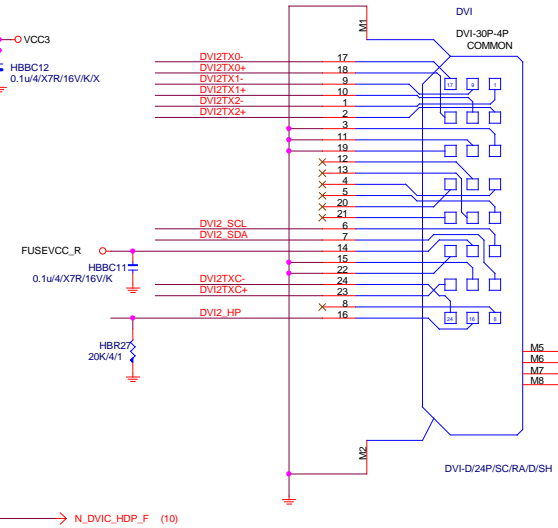
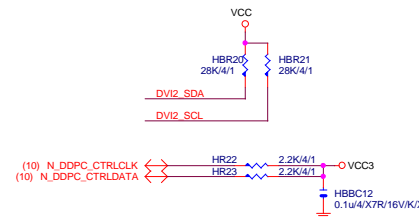
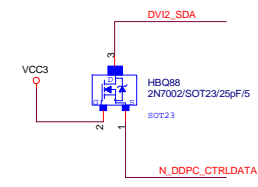
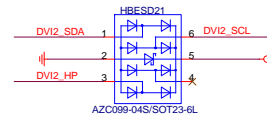
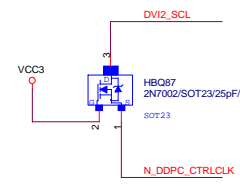
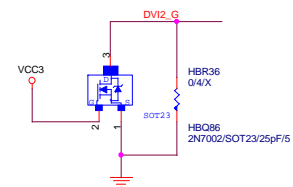
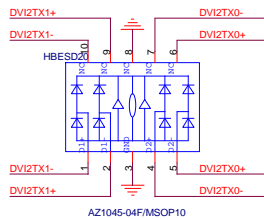
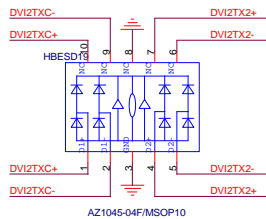
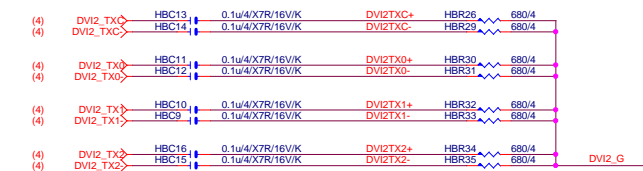
【技術通報R&D技術通報151】
33ohm Change to 68ohm

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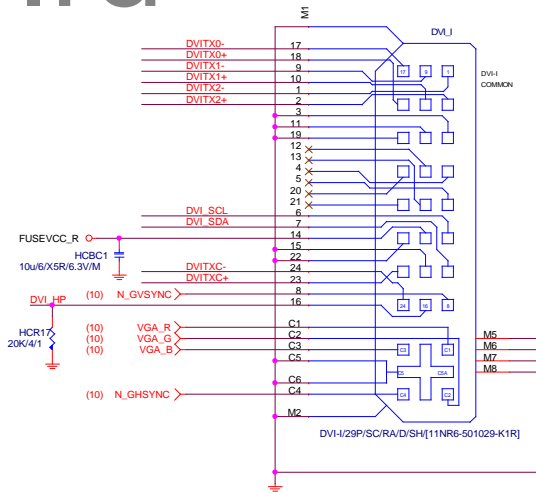
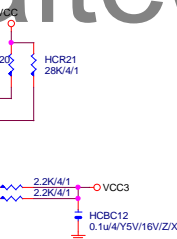
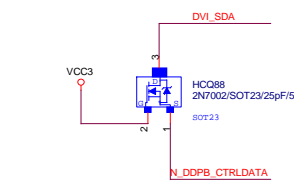
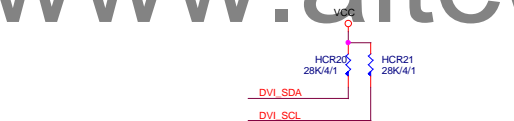
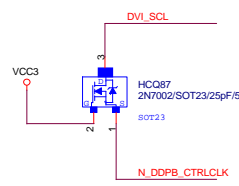
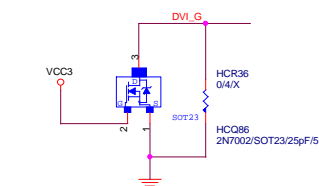
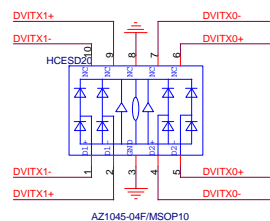
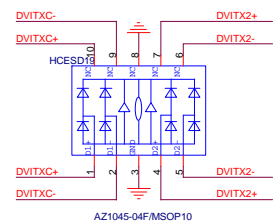


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